

	SPECIFICATIONS					
CUSTOMER	· PTC					
SAMPLE CODE	SH800480T0	SH800480T013-IBB01				
MASS PRODUCTION CODE	- PH800480T0	13-IBB01				
SAMPLE VERSION	- 01					
SPECIFICATIONS EDITION	. 004					
DRAWING NO. (Ver.)	JLMD-PH800					
PACKAGING NO. (Ver.)	JPKG-PH800	JPKG-PH800480T013-IBB01_002				
	Customer Approved	Date: JS RD APPROVED				
Approved	Checked	Designer				
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Preliminary specification for design input

Specification for sample approval

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History of Version

Date (mm / dd / yyyy)	Ver.	Edi.	Description	Page	Design by
02/28/2015	01	001	New Drawing	-	張斌
04/09/2015	01	002	New Sample	-	張斌
05/19/2015	01	003	Modify LCM Drawing	Appendix	張斌
08/25/2015	01	004	Show Backlight Life Time	9	張斌
					V

Total: 30 Page



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LCM Drawing LCM Packaging



1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	800 * (RGB) * 480
LCD Type	a-Si TFT, Normally white, Transmissive type
Screen size(inch)	7.0 inch
Viewing Direction	6 O'clock
Backlight Type	LED B/L
Weight	-
Interface	RGB Interface
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer website :
	http://www.powertip.com.tw/news.php?area_id_view=1085560481/

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	164.9 (W) * 100.0 (L) *4.95 (H)	mm

LCD panel

Item	Standard Value	Unit
Active Area	154.08 (W) * 85.92 (L)	mm

Note : For detailed information please refer to LCM drawing.



1.3 Absolute Maximum Ratings

Module

Item	Symbol	Condition	Min.	Max.	Unit	Remark
	DV_DD		-0.3	5.0	V	
	AVdd	GND=0	6.5	13.5	V	
Power Supply Voltage	V_{GH}		-0.3	40	V	
	V_{GL}	AGND=0	-20	0.3	V	
	V _{GH} - V _{GL}	-	0	40	V	
Operating Temperature	T _{OP}	-	-20	70	°C	
Storage Temperature	Тѕт	-	-30	80	°C	

The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

1.4 DC Electrical Characteristics

GND = 0V, $Ta = 25^{\circ}C$

Item	Symbol	Min.	Тур.	Max.	Unit	Remark
	DVDD	3.0	3.3	3.6		
Supply Voltage	V _{GH}	15.3	16.0	16.7	V	
Supply Voltage	Vgl	-7.7	-7.0	-6.3	v	
	AVdd	10.2	10.4	10.6		-
VCOM	Vсом	-	3.9	-	V	
Input signal Voltage	Vih	$0.7 DV_{DD}$	-	DV_DD	v	
input signal voltage	VIL	0	-	0.3DVdd	v	
	I (DVdd)	-	3.0	-		Pattern= Full display
		-	3.5	5.0		Pattern= Red *1
Supply Current	I (AVdd)	-	15	-	mA	Pattern= Full display
	T (AVDD)	-	17.5	20	IIIA	Pattern= Red
	Ідн	-	0.15	0.3		Pattern= Red
	lg∟	-	0.15	0.3		Pattern= Red

Note1: Maximum current display.



1.5 Optical Characteristics

TFT LCD Module

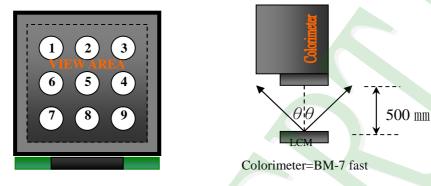
DV_{DD} = 3.3 V, Ta=25°C

ltem		Symbol	Condition	Min.	Тур.	Max.	unit	
Deenenee time	Rise	Tr	Ta = 25°C	-	10	20	100.0	Note 2
Response time	Fall	Tf	$\theta X, \theta Y = 0^{\circ}$	-	15	30	ms	Note 2
	Тор	θY+		40	50	-		
	Bottom	θY-	CR ≥ 10	60	70	-	Dog	Note 1
Viewing angle	Left	θX-	CR 2 10	60	70	-	Deg.	Note 4
	Right	θX+		60	70	-		
Contrast rati	0	CR		400	500	-		Note 3
	\//bita	Х		0.25	0.30	0.35		
	White	Y		0.29	0.34	0.39		
	Ded	Х		0.53	0.58	0.63		
Color of CIE Coordinate	Red	Y	Ta = 25°C θX , θY = 0°	0.30	0.35	0.40		Note1
(With B/L&TP)	Croop	Х	0, 1, 0, 1 = 0	0.30	0.35	0.40	-	Note I
(Green	Y		0.54	0.59	0.64		
	Blue	Х		0.10	0.15	0.20		
	Diue	Y		0.02	0.07	0.12		
Average Brightr	ness			,				
Pattern=white di	splay	IV	lf= 160 mA	260	400		cd/m ²	Note1
(With B/L&TP)*1							
Uniformity (With B/L&TP))*2	∆B	-	70	-	-	%	Note1



Note 1:

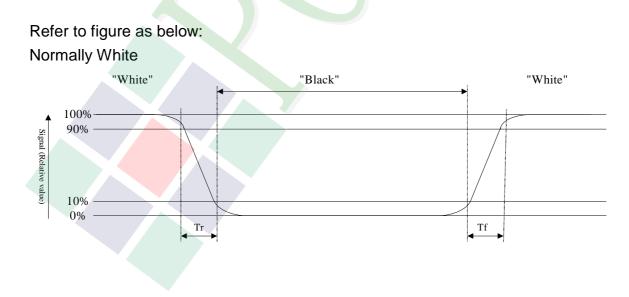
- *1 : △B=B(min) / B(max) * 100%
- *2 : Measurement Condition for Optical Characteristics:
 - a : Environment: 25°C±5°C / 60±20%R.H , no wind , dark room below 10 Lux at typical lamp current and typical operating frequency.
 - b : Measurement Distance: 500 ± 50 mm \rightarrow (0= 0°)
 - c : Equipment: TOPCON BM-7 fast , (field 1°) , after 10 minutes operation.
 - d: The uncertainty of the C.I.E coordinate measurement ±0.01 , Average Brightness ± 4%



To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

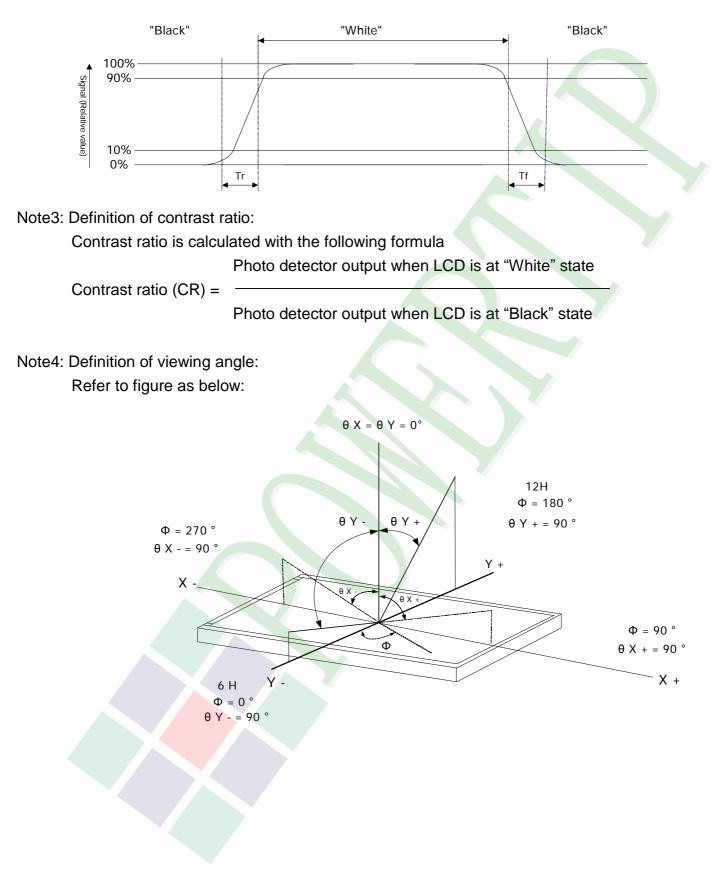
Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.





Normally Black





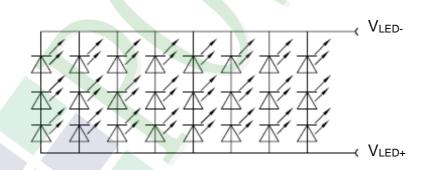
1.6 Backlight Characteristics

Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
LED Forward Current	IF		-	30*8	mA
LED Reverse Voltage	VR	Ta =25℃	-	5	V
Power consumption	Pd			90*24	mW

Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF		9.0	9.6	10.2	
Average Brightness (Without LCD &T/P)	IV	lf= 160 mA	8600	10300	13800	cd/m ²
CIE Color Coordinate	Х		0.25	0.28	0.31	
(Without LCD &T/P)	Y		0.26	0.29	0.32	-
Color			White			



Other Description

Item	Conditions	Description
Life Time	Ta =25℃ IF= 160mA	20000 hrs



1.7 Touch Panel Characteristics

1.7.1 Optical Characteristics

ltem	Specification
1.Transparency	80% Min

1.7.2 Mechanical Characteristic

Item	Specification
1.Input Method	Finger or stylus pen
2.Hardness of surface	3H -pressure 500g of ,45deg.
3.Activation Force	250gf less individual point with stylus pen(R0.8)
	Activation force guarantee area:3.0mm inside of Active Area.
4.Linearity Force	150gf less input with stylus pen(R0.8)
	Activation force guarantee area:3.0mm inside of Active Area.

1.7.3 Electrical Characteristics

Item	Specification				
1.Rated Voltage	DC 5V(DC 7V Max)				
2.Resistance Between	Direction X (Glass side): 500Ω~ 1000Ω				
Terminals.	Direction Y (Film side): 100Ω~ 500Ω				
3.Insulation Resistance	20 M Ω or more (DC 25 V 1min)				
4.Linearity	±1.5%.				
	Linearity(%)= ΔV/(EV-SV)*100.				
	ΔV : The difference between the ideal voltage and measured voltage on the each measuring line.				
	SV: Voltage of starting Points.				
	EV: Voltage of Ending Points.				
	(Test condition refers to 1.7.2 item4)				
5.Bouncing <10ms (Tip R 3.75mm, hardness 10°~20°, silicon rubber ,500gf					
	operation : 40 mm/sec)				

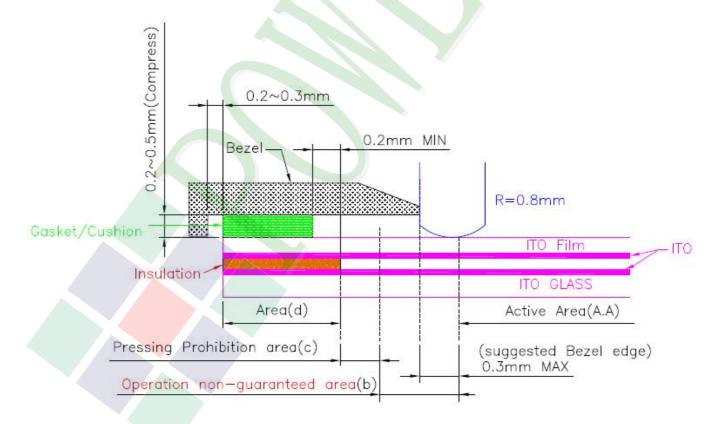


1.7.4 Reliability Characteristic

NO	Test Item	Test Condition	Test Result
	Hitting Durability	1,000,000times min.(R 8 mm	Follow 1.7.3 item2 and item4.
1		Silicon Rubber Hardness 60°	
		250gf 2times/sec).	
2	Pen Sliding Durability	100,000 times min(Tip R0.8mm).	Follow 1.7.3 item2 and item4.
		ψ9mm steel ball is dropped on the	No Crack
3	Impact Resistance	surface from 30 cm height at 1	
		time.	
	Flexible pattern Bending	Bending 3 times by bending	Follow 1.7.3 item2.
4	Resistance	radius R1.0 mm	



- 1.7.5 Touch Panel Design/Handing Guide
- (1) Keep the gap, for example 0.2 to 0.3mm, between bezel edge and T/P edge. The reason is to avoid the bezel edge from contacting T/P surface that may cause "short" with bottom layer
- (2) Insertion a cushion material is recommended.
- (3) The cushion material should be limited on the busbar insulation paste area. If it is over the transparent insulation paste area, a "short" may be occurred.
- (4) Do not to use an adhesive tape to bond it on the front of T/P and hang it to the housing bezel.
- (5) Never expand the T/P top layer (PET Film) like a balloon by internal air pressure. The life of the T/P will extremely decreasing.
- (6) Top layer, PET, dimension is changing base on environment temperature and humidity. Please avoid a stress from housing bezel to top layer, because it may cause "waving".
- (7) The input to the Touch Panel sometimes distorts touch panel itself.
- (8)To use the stylus pen or fingernail sliding at the edge of the housing is prohibited. It would cause the cracking of the ITO coating and damage the touch panel. It also request not to press this area while assembling
- (9) Purpose: In order to prevent accidental use and performance deterioration, please keep the following precautions.



In order to prevent unusual performance degradation and malfunction of a touch panel, please carry out the set case designing and a touch panel assembling method after surely considering the definition of each area illustrated in above figure.



Area(a) : Active area

The active area is guaranteed the position data detectable precision, operation force and other operations. it is strongly recommended to place the operation button or menu keys within the active area. Due to structure, the active area is less durable at the edge or close to the edge.

Area(b) : Operation non-guaranteed area

This area does not guarantee a touch panel operation and its function. When this area is pressed, touch panel shows degradation of its performance and durability such as a pen sliding durability becomes about one-tenth compared with the active area (area-(a) as guaranteed area) and its operation force requires about double. About 0.5 mm outside from a boundary of the active area corresponds to this area.

Area(c) : Pressing prohibition area

The area which forbids pressing, because an excessive load is applied to a transparent electrode (ITO) and a serious damage is given to a touch panel function by pressing. About 0.5 mm outside from Operation non-guaranteed area.

Area(d) : Non-Active area The area does not activate even if pressed.



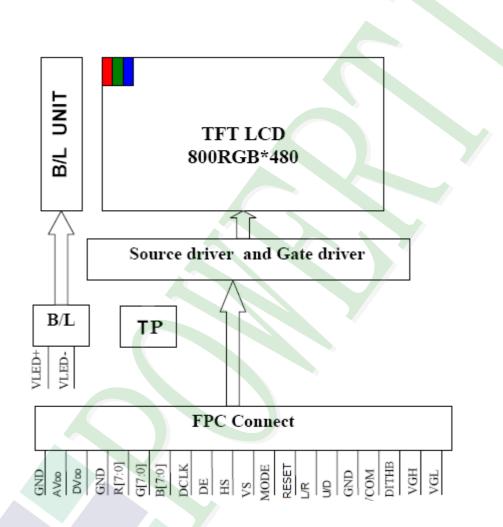
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram





2.2 Interface Pin Description

Pin NO.	SYMBOL	DESCRIPTION	Type:Remark
1	VLED+	Power For LED backlight (+).	Power
2	VLED+	Power For LED backlight (+).	Power
3	VLED-	Power For LED backlight (-).	Power
4	VLED-	Power For LED backlight (-).	Power
5	GND	Power ground.	Power
6	Vcom	Common voltage.	
7	DVDD	Power for Digital Circuit.	
8	MODE	DE/SYNC mode select.	I,Note 1
9	DE	Data Input Enable.	I
10	VS	Vertical Sync Input.	I
11	HS	Horizontal Sync Input.	I
12	B7	Blue Data(MSB).	
13	B6	Blue Data.	
14	B5	Blue Data.	I
15	B4	Blue Data.	
16	B3	Blue Data.	I
17	B2	Blue Data.	I
18	B1	Blue Data.	I:Note 2
19	B0	Blue Data(LSB).	I:Note 2
20	G7	Green Data(MSB).	
21	G6	Green Data.	I
22	G5	Green Data.	I
23	G4	Green Data.	
24	G3	Green Data.	I
25	G2	Green Data.	
26	G1	Green Data.	I:Note 2
27	GO	Green Data(LSB).	I:Note 2
28	R7	Red Data(MSB).	I
29	R6	Red Data.	
30	R5	Red Data.	I
31	R4	Red Data.	I
32	R3	Red Data.	I
33	R2	Red Data.	I
34	R1	Red Data.	I:Note 2
35	R0	Red Data(LSB).	I:Note 2
36	GND	Power Ground	Power
37	DCLK	Sample clock	I:Note 3



38		DESCRIPTION	Type:Remark
00	GND	Power Ground.	Power
39	L/R	Left / right selection.	I:Note 4
40	U/D	Left / right selection.	I:Note 4
41	V _{GH}	Gate On Voltage.	Power
42	V _{GL}	Gate OFF Voltage.	Power
43	AV _{DD}	Power for Analog Circuit.	Power
44	RESET	Global reset pin.	I:Note 5
45	NC	No connection.	-
46	Vсом	Common Voltage.	I
47	DITHB	Dithering Function.	I:Note 6
48	GND	Power Ground.	Power
49	NC	No connection.	-
50	NC	No connection.	-

l:input

Note 1: DE/SYNC mode select. Normally pull high.

When select DE mode, MODE="1", VS and HS must pull high.

When select SYNC mode, MODE= "0", DE must be grounded.

Note 2: When input 18 bits RGB data, the two low bits of R,G and B data must be grounded.

Note 3: Data shall be latched at the falling edge of DCLK.

Note 4: Selection of scanning mode.

	0	
Setting of sca	an control input	Scanning direction
U/D	L/R	
GND	DVpp	Up to down, left to right
DVpp	GND	Down to up, right to left
GND	GND	Up to down, right to left
DVpp	DVpp	Down to up, left to right

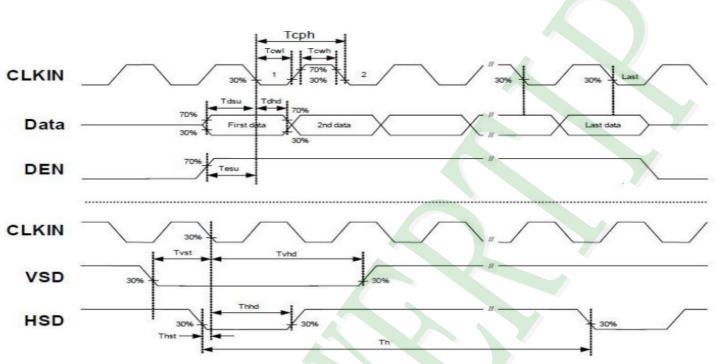
Note 5: Global reset pin. Active low to enter reset state. Suggest to connect with an RC reset circuit for stability. Normally pull high.

Note 6: Dithering function enable control, normally pull high. When DITHB="1",Disable internal dithering function. When DITHB="0",Enable internal dithering function.



2.3 Timing Characteristics

2.3.1 Signal AC Characteristics



lt and	0 mm had		Values		11	Description
Item	Symbol	Min	Тур	Max.	Unit	Remark
HS setup time	Thst	8	-	-	ns	
HS hold time	Thhd	8	-	-	ns	
VS setup time	Tvst	8	-	-	ns	
VS setup time	Tvhd	8	-	-	ns	
VS setup time	Tdsu	8	-	-	ns	
VS setup time	Tdhd	8	-	-	ns	
DE setup ti <mark>me</mark>	Tesu	8	-	-	ns	
DE hole time	Tehd	8	-	-	ns	
DVDD Power On Slew rate	TPOR	-	-	20	ms	From 0 to 90%DVDD
RESET pulse width	TRst	1	-	-	ms	
DCLK cycle time	Tcoh	20	-	-	ns	
DCLK pulse duty	Tcwh	40	50	60	%	

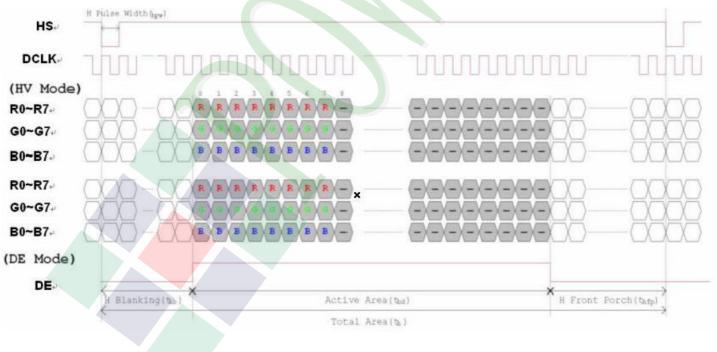


2.3.2 Input Timing Setting

Item	Symbol	Values			Unit	Remark
		Min.	Тур.	Max.		
Horizontal Display Area	Thd		800		DCLK	
DCLK Frequency	Fclk	26.4	33.3	46.8	MHz	
One Horizontal Line	Th	862	1056	1200	DCLK	
HS pulse width	Thpw	1		40	DCLK	
HS Blanking	Thb	46	46	46	DCLK	
HS Front Porch	Thfp	16	210	354	DCLK	

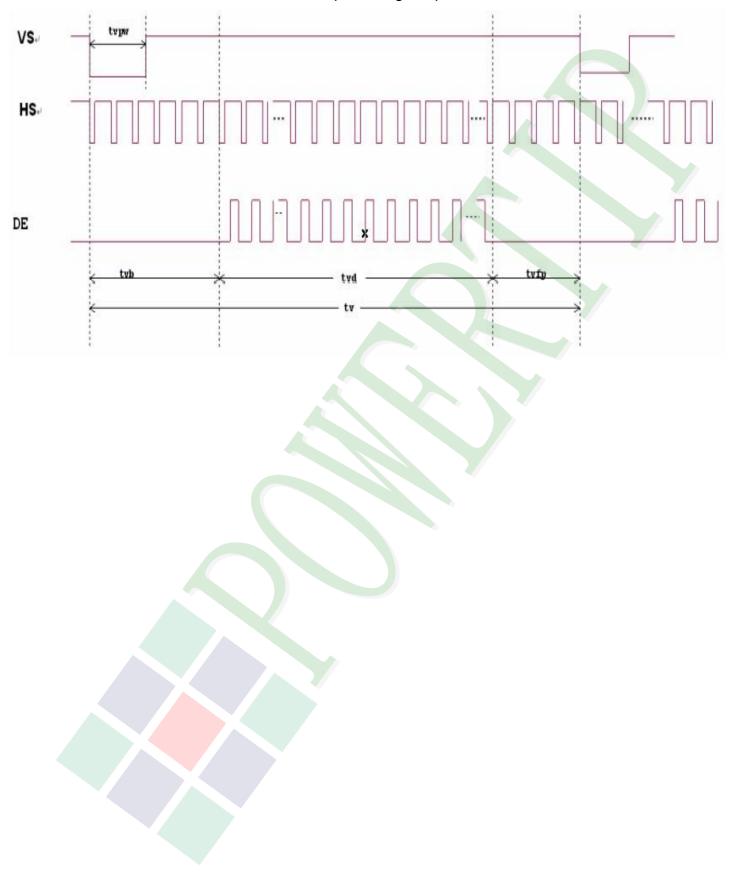
Item	Symbol	Values		Unit	Remark	
		Min.	Тур.	Max.		
Vertical Display Area	Tvd		480		ТН	
VS period time	Τv	510	525	650	ΤH	
VS pulse width	Tvpw	1		20	TH	
VS Blanking	Tvb	23	23	23	TH	
VS Front Porch	T∨fp	7	22	147	TH	

Horizontal input timing diagram





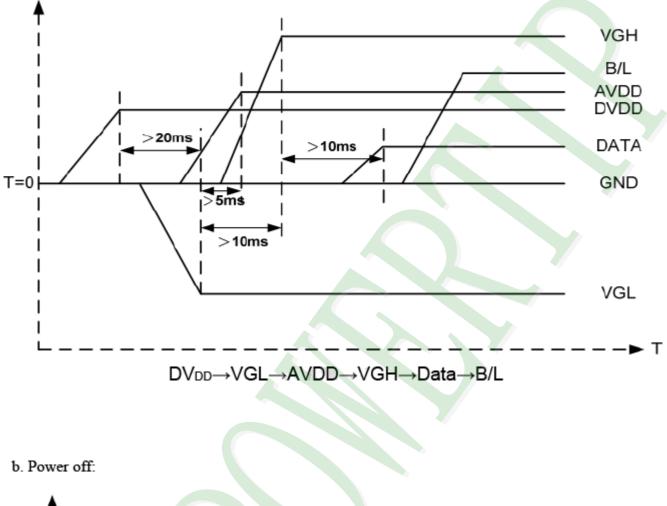
Vertical input timing diagram

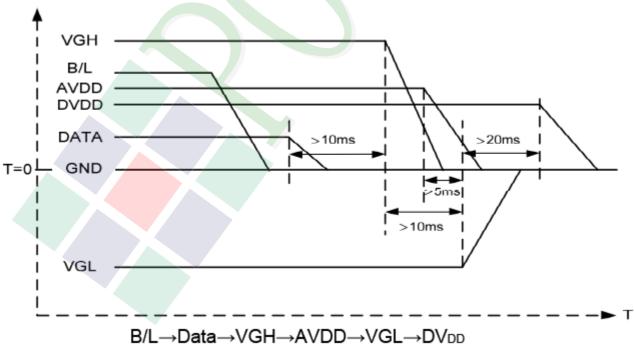


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2.3.3 Power On/Off Characteristics

a. Power on:

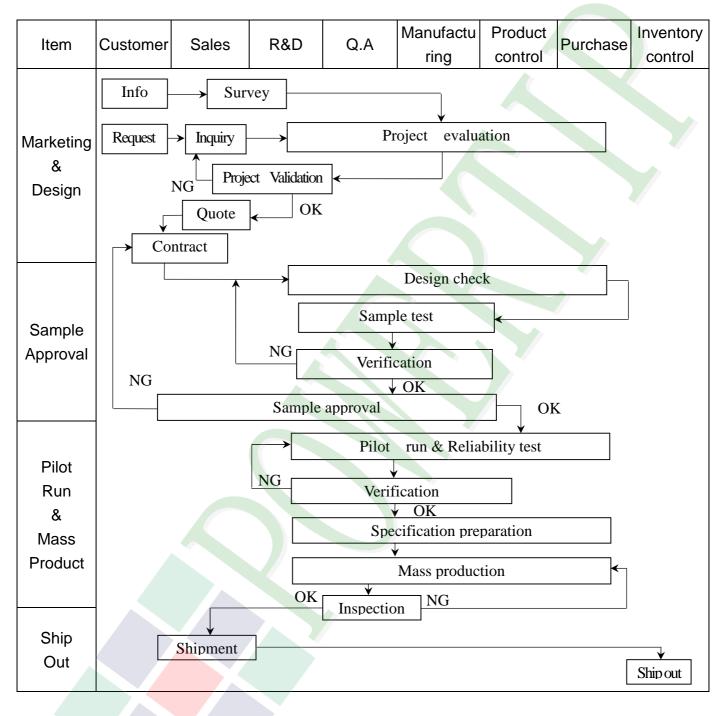






3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart



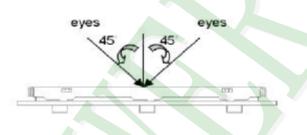


Item	Customer	Sales	R&D	Q.A	Manufact uring	Product control	Purchase	Inventory control
Sales Service	Info	→ Claim sis report	[Trackin	Failure an Corrective			
Q.A Activity	1. ISO 900 3. Equipme 5. Standard		ion	4	Process in . Education			es

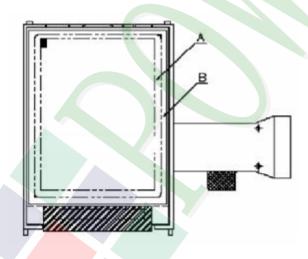
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3.2. Inspection Specification

- Scope : The document shall be applied to TFT-LCD Module for 3, 5" ~10" (Ver.B01).
- ◆Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.
- ◆Equipment : Gauge 、 MIL-STD 、 Powertip Tester 、 Sample
- ◆Defect Level : Major Defect AQL : 0.4 ; Minor Defect AQL : 1.5
- ♦OUT Going Defect Level : Sampling.
- Standard of the product appearance test :
 - a. Manner of appearance test :
 - (1). The test best be under 20W×2 fluorescent light , and distance of view must be at 30 cm.
 - (2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



A area : viewing area

B area : Outside of viewing area

(4). Standard of inspection : (Unit : mm)



◆Specification For TFT-LCD Module 3. 5″~10″: (Ver.B01)									
NO	Item		Criter	ion	Level				
		1. 1The part number is inconsistent with work order of production.							
01	Product condition	1.2 Mixed prod	1. 2 Mixed product types.						
		1.3 Assembled i	n inverse direction.		Major				
02	Quantity	2, 1 The quantity	The quantity is inconsistent with work order of production.						
03	Outline dimension	3.] Product dir diagram.	3.] Product dimension and structure must conform to structure diagram.						
	Electrical Testing	4.1 Missing line	character and icor	n.	Major				
		4. 2 No function or no display.							
04		4. 3 Display malfunction.							
		4.4 LCD viewing angle defect.							
		4.5 Current con	sumption exceeds	product specifications.	Major				
			Item	Acceptance (Q'ty)					
	Dot defect		Bright Dot	≤ 4					
	Dot defect	Dot	Dark Dot	≦ 5					
	(Bright dot 🕥	Defect	Joint Dot	≦ 3					
05	Dark dot)		Total	≦ 7	Minor				
		5, 2 It is defined	blue scree	ect area $>1/2$ dot.	and				



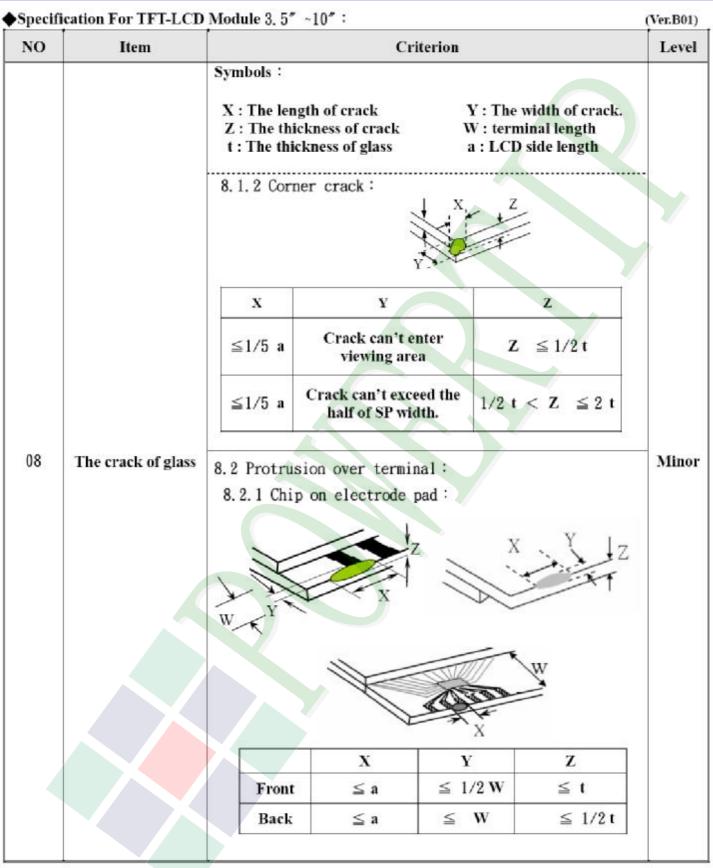
♦ Specif	fication For TFT-LCD	Module 3. 5″~10″ :			(Ver.B01)
NO	Item	Crit	terion		Level
	6.	l Round type (Non-display or	display) :		
		Dimension (diameter ÷ Φ)	Acceptance A area	e (Q'ty) B area	
	Black or white dot < scratch <	$\Phi \leq 0.25$	Ignore		
	contamination	$0.25~<~\Phi\leq 0.50$	5		
	Round type	$\Phi > 0.50$	0	Ignore	
		Total	5		
06	6. 2 Line type(Non-display or display): $\Phi = (x+y)/2$				
	Line type	Length (L) Width (V	V) Accep	otance (Q'ty) ea Barea	
	∼∕ [‡] ^w		≤ 0.03 Ignor	re	
	→ T. I	$L \le 10.0$ $0.03 < W \le$			
		$L \leq 5.0 \qquad 0.05 < W \leq$		Ignore	
		W	>0.10 As rou type		
		Total	5		
		Dimension (diameter∶Φ) -	Acceptanc A area	e (Q'ty) B area	
		$\Phi \leq 0.25$	Ignore		
07	Polarizer	$0.25 < \Phi \le 0.50$	4		Minor
	Bubble	$0.50 < \Phi \leq 0.80$	1	Ignore	
		$\Phi > 0.80$	0		
		Total	5		



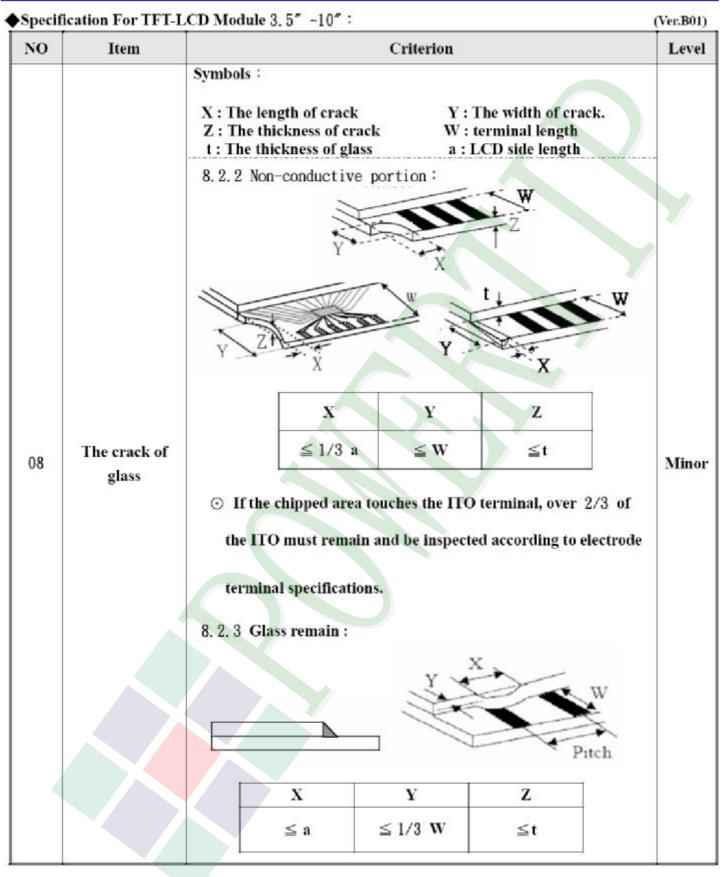
◆Specification For TFT-LCD Module 3. 5″~10″:

◆Specification For TFT-LCD Module 3. 5″~10″ : (Ver.B01				
NO	Ttem	Criterion	Level	
		Symbols :X : The length of crackZ : The thickness of crackt : The thickness of glassA : LCD side length		
		8.1 General glass chip : 8.1.1 Chip on panel surface and crack between panels:		
		V Z Z X Y		
08	The crack of glass		Minor	
		Seal width		
		X Y Z		
		$\leq a$ Crack can't enter $\leq 1/2 t$ viewing area		
		$\leq a$ Crack can't exceed the $1/2 t < Z \leq 2 t$ half of SP width.		











4. RELIABILITY TEST

4.1 Reliability Test Condition

(Ver.B01)

•••.	Reliability lest collu		
NO.	TEST ITEM	TEST CONDITION	
1	High Temperature Storage Test	Keep in +80 ±2°C 96 hrs Surrounding temperature, then storage at normal condition 4hrs.	
2	Low Temperature Storage Test	Keep in -30 ±2℃ 96 hrs Surrounding temperature, then storage at normal condition 4hrs.	
3	High Temperature / High Humidity Storage Test	Keep in +60 °C / 90 % R.H duration for 96 hrs Surrounding temperature, then storage at normal condition 4 hrs. (Excluding the polarizer)	
4	Temperature Cycling Storage Test	$\begin{array}{cccc} -30^{\circ}\text{C} & \rightarrow & +25^{\circ}\text{C} & \rightarrow & +80^{\circ}\text{C} & \rightarrow & +25^{\circ}\text{C} \\ (30\text{mins}) & (5\text{mins}) & (30\text{mins}) & (5\text{mins}) \\ & & & & & \\ \hline & & & & & \\ \hline & & & & &$	
5	ESD Test	Air Discharge: Apply 2 KV with 5 timesContact Discharge: Apply 250 V with 5 times discharge for each polarity +/-1. Temperature ambiance : 15°C ~35°C2. Humidity relative : 30%~60%3. Energy Storage Capacitance(Cs+Cd) : 150pF±10%4. Discharge Resistance(Rd) : 330 Ω±10%5. Discharge, mode of operation : Single Discharge (time between successive discharges at least 1 sec) (Tolerance if the output voltage indication : ±5%)	
6	Vibration Test (Packaged)	 Sine wave 10~55 Hz frequency (1 min) The amplitude of vibration :1.5 mm Each direction (X \ Y \ Z) duration for 2 Hrs 	
7	Drop Test (Packaged)	Packing Weight (Kg) Drop Height (cm) 0 ~ 45.4 122 45.4 ~ 90.8 76 90.8 ~ 454 61 Over 454 46	



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes , please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands , this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is 320±10°C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM .

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}C \pm 5^{\circ}C$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush , shake , or jolt the module.

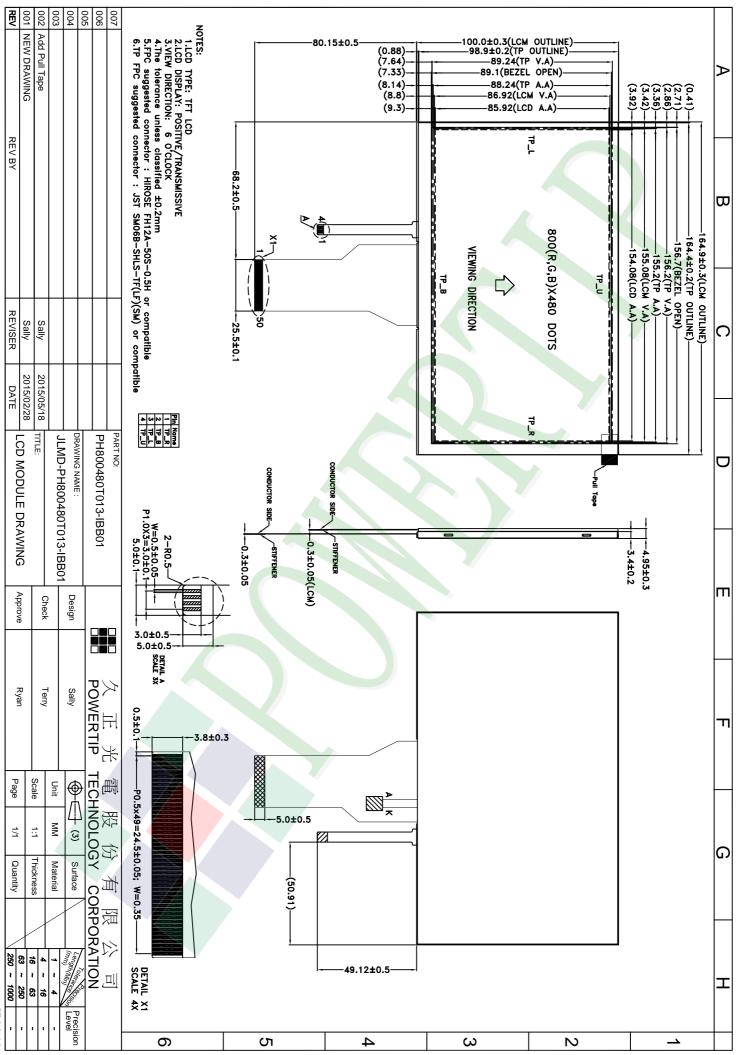
5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

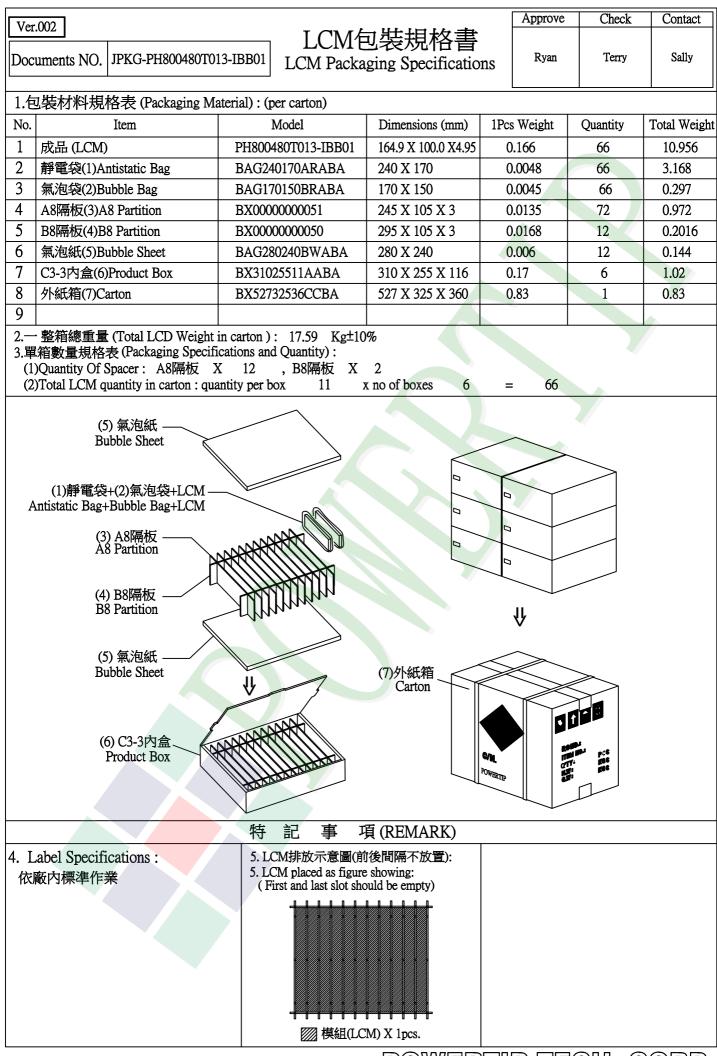
The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



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POWERTIP TECH. CORP.