

DATASHEET & RELIABILITY DATA

LTS5 SERIES

(주)오디피

Open Digital Power Corp.

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MAX. Power 5W Isolated On-Board Type AC-DC Converter

LTS5 Series Isolated On-Board Type AC-DC Converter



Features

- Power Saving Mode Operation
- High Efficiency
- Isolated Input - Output
- Built-in over current protection circuit
- Short Circuit Protection
- Universal Input Voltage(Free Voltage)
- RoHS compatible design

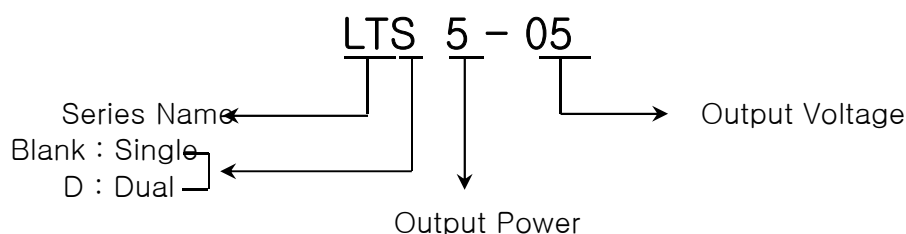
Applications

- Data and telecommunication
- FA control
- Datacommunication electronic equipments

Environment

- Operating Temperature : -20°C ~ 70°C
- Operating Humidity : 20% ~ 90% RH (Non condensing)
- Storage Temperature : -40°C ~ 85°C
- Cooling : Free-Air Convection
- MTBF : 5.7 x 10⁵ hrs

Model Name Structure



Pin assignments & Function



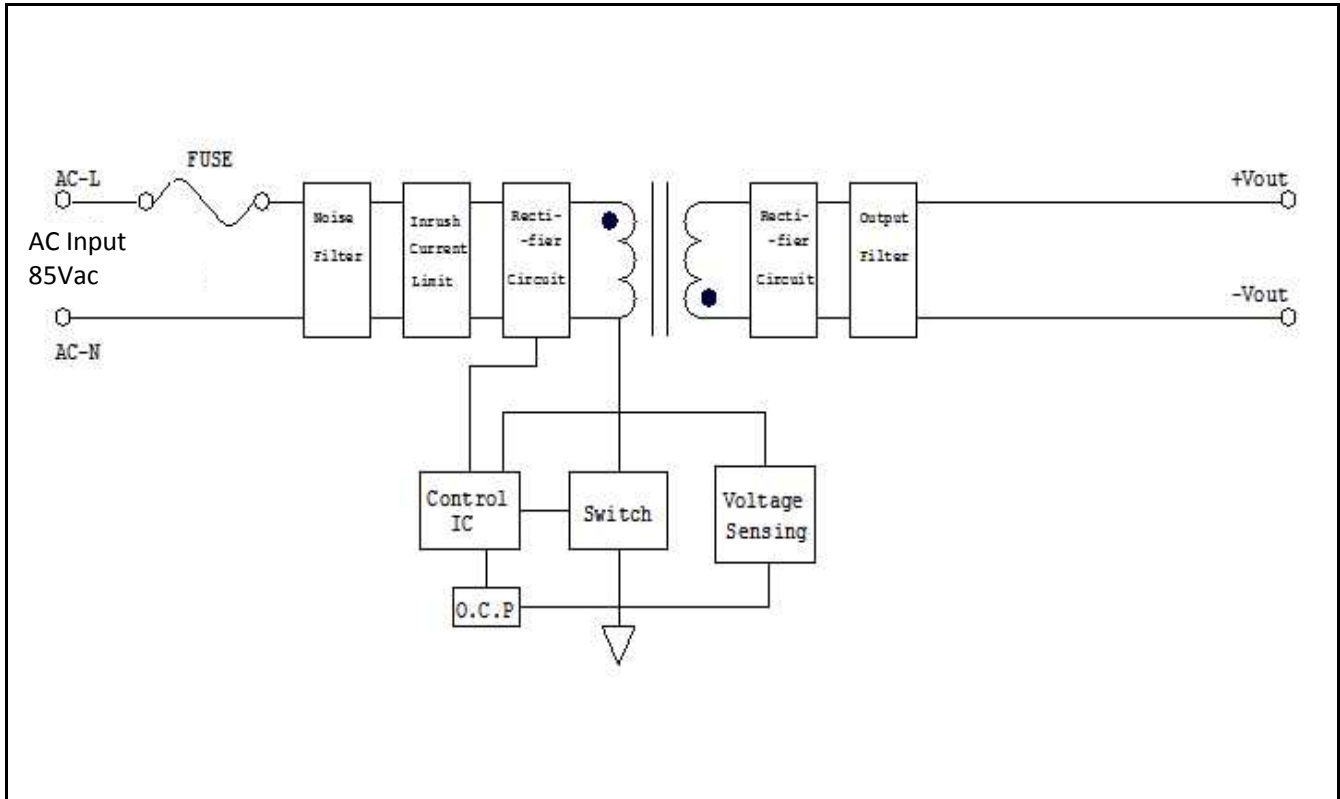
<Top View>

- Single Output Name & Function

PIN No.	Single
1	AC input N
2	AC input L
3	+ Vout
4	- Vout

– Datasheet

1. Internal Circuit Architecture



2. Maximum Ratings

Characteristics		Symbol	Min.	Typ.	Max.	Unit
No-load Power Consumption	LTS5 - XX				0.3	W
Frequency	LTS5 - XX		47	–	63	Hz
Input Voltage Continuous	LTS5 - 05	Vin	85	–	264	Vac
	LTS5 - 12		85	–	264	
	LTS5 - 15		85	–	264	
	LTS5 - 24		85	–	264	
Operating Ambient Temperature		Ta	–20	–	70	°C
Storage Temperature		Tstg	–40	–	85	°C
Withstand Voltage (Input - Output)			–	–	3000	Vac

3. Electrical Characteristics

– Input Section

Ta : 25°C, Vin : Typical Input Voltage

Characteristics		Symbol	Min.	Typ.	Max.	Unit
No-load Power Consumption	LTS5 – XX				0.3	W
Operating Voltage Range	LTS5 – XX	Vin	85	110, 220	264	Vac
Frequency	LTS5 – XX		47	50, 60	63	Hz
Max. Input Current (Vin : rated, Io : 100%)	LTS5 – XX	Iin		0.15		A
Max. No Load Input Current (Vin : min.)	LTS5 – XX				4	mA
Inrush Current (In : 220Vac)	LTS5 – XX				30	A
Leakage Current (In : 220Vac)	LTS5 – XX				0.35	mA

– Output Section

Ta : 25°C, Vin : Minimum, Typical, Maximum Input Voltage

Characteristics		Symbol	Min.	Typ.	Max.	Unit
Output Voltage Accuracy	Single	Vo	–	–	±2	%
			–	–		
Regulation	Line Regulation (From min. Vin to max. Vin, constant load)		–	–	±1	%
	Load Regulation (From no load to maximum load)		–	–	±1	%(@Single)
Output Ripple and Noise (Vin : Rated, Io : Max., BW : 20MHz)		mVp-p	100	–	200	mV (peak to peak)

Characteristics		Symbol	Min.	Typ.	Max.	Unit
Output Current	LTS5 - 05	I _o	-	-	1.0	A
	LTS5 - 12		-	-	0.42	
	LTS5 - 15		-	-	0.34	
	LTS5 - 24		-	-	0.21	
Output Current Limit (OCP : Over Current Protection, recovers automatically)			105	-	-	%
Dynamic Load Response (V _{in} : rated, I _o : from 10% to 100%, from 100% to 10%, BW : 20MHz, Freq. : 100Hz, Duty : 0.5, Tr/Tf : 100us)			-	-	3% of V _{out}	mV (peak to peak)
Start - Up Time		T _{start}			100	ms
Turn - on Overshoot			-	-	5	%
Efficiency (V _{in} : Rated, I _o : Max.)	LTS5 - 05		-	72	-	%
	LTS5 - 12		-	77	-	
	LTS5 - 15		-	78	-	
	LTS5 - 24		-	80	-	

4. Isolation Characteristics

Characteristics		Symbol	Min.	Typ.	Max.	Unit
Withstand Voltage	Input - Output		-	-	3000	V _{ac}
Isolation Resistance (DC500V at 25°C and 70%RH)	Input - Output	R _{iso}	100	-	-	MΩ

5. General Characteristics

Characteristics	Symbol	Min.	Typ.	Max.	Unit
Remote on / off control (CNT Pin, Negative Logic Module on : Logic Low or Short to -Vin Module off : Logic High or open)	CNT				
External Trim Adj. Range (TRM Pin, Vout variation by external parts)	TRM				
Switching Frequency					kHz
MTBF (MIL-HDBK-217F)		5.7 x 10 ⁵			hrs
Dimension (L x W x H)		58.0 x 30.0 x 19.0			mm
Weight		-	-	50	grams

6. Environment

Characteristics	Symbol	Min.	Typ.	Max.	Unit
Operating Temperature Range	Ta	-20	-	70	°C
Operating Humidity (non Condensing)		20	-	90	%RH
Storage Temperature	Tstg	-40	-	85	°C

– Reliability Data

1. MTBF

Calculating Reliable Values of MTBF

Calculated based on part count reliability projection of MIL-HDBK-217F individual failure rates λ_g is given to each part and MTBF is calculated by the count of each part.

Method is :

$$MTBF = \frac{10^6}{\sum_{i=1}^n Ni(\lambda_g \cdot \pi Q)_i} = \frac{10^6}{\lambda_{equip}} \quad [\text{hours}]$$

For a given equipment environment where :

λ_{equip} = Total equipment failure rate (Failures / 10⁶ Hours)

λ_g = Generic failure rate for the i th generic part (Failures / 10⁶ Hours)

πQ = Quality factor for the i th generic part ($\pi Q = 1$)

Ni = Quantity of i th generic part

n = Number of different generic part categories in the equipment

	PART	Number	Failure Rate	Failure Rate
1	Logic IC	1	0.015	0.0150000
2	FET	0	0.012	–
3	Voltage Regulaor	0	0.002	–
4	Diode (Zener)	0	0.002	–
5	Diode (FRD)	2	0.069	0.1380000
6	Diode (SBD)	1	0.027	0.0270000
7	Bridge Diode	1	0.066	0.0660000
8	LED	0	0.00023	–
9	Varistor	0	0.0013	–
10	Photo-coupler	0	0.07	–
11	Thyristor	0	0.0022	–
12	Elec.- Cap.	4	0.019	0.0760000
13	Ceramic Cap.	1	0.026	0.0260000
14	MLCC	5	0.053	0.2650000
15	Choke coil	3	0.00022	0.0006600
16	Switching transformer	1	0.0042	0.0042000
17	Line Filter	0	0.0044	–
18	Resistor	7	0.0024	0.0168000
19	Resistor Variable	0	0.0024	–
20	Thermister	0	0.0019	–
21	Connertor	4	0.052	0.2080000
22	Soldering Point	68	0.0078	0.5304000
23	PCB	1	0.37	0.3700000
24	Fuse	1	0.01	0.0100000
Total Equipment Failure Rate (λ_{equip})				1.7530600
MTBF = 10 ⁶ / λ_{equip} (F/T)				570,431.132
MTBF ≅ 570,000[Hours]				

2. Lifetime

The shortest lifetime parts is an electrolytic capacitor. Thus, the lifetime of SMPS is lifetime of electrolytic capacitors.

Lifetime of electrolytic capacitor can be calculated by the following factors.

- T_0 : Load life rating
- T_{max} : Maximum temperature rating of capacitor
- T_{case} : Temperature of case

$$T[\text{hour}] = T_0 * 2^{\frac{T_{max} - T_{case}}{10}}$$

T : Life Time(Electrolytic Cap.)
 T_0 : Load Life Rating
 T_{max} : Max. Temperature rating of capacitor
 T_{case} : Temperature of case

LTS5-05 <(5VDC, 1A(100% Load)>, [unit : hrs]			
110Vac Input		220Vac Input	
$T_{amb.} : 25^{\circ}\text{C}$	$T_{amb.} : 50^{\circ}\text{C}$	$T_{amb.} : 25^{\circ}\text{C}$	$T_{amb.} : 50^{\circ}\text{C}$
35,000 hrs	7,000 hrs	33,000 hrs	6,500 hrs

LTS5-12 <(12VDC, 0.42A(100% Load)>, [unit : hrs]			
110Vac Input		220Vac Input	
$T_{amb.} : 25^{\circ}\text{C}$	$T_{amb.} : 50^{\circ}\text{C}$	$T_{amb.} : 25^{\circ}\text{C}$	$T_{amb.} : 50^{\circ}\text{C}$
85,000 hrs	15,000 hrs	91,000 hrs	16,000 hrs

LTS5-15 <(15VDC, 0.34A(100% Load)>, [unit : hrs]			
110Vac Input		220Vac Input	
$T_{amb.} : 25^{\circ}\text{C}$	$T_{amb.} : 50^{\circ}\text{C}$	$T_{amb.} : 25^{\circ}\text{C}$	$T_{amb.} : 50^{\circ}\text{C}$
65,000 hrs	12,000 hrs	52,000 hrs	10,000 hrs

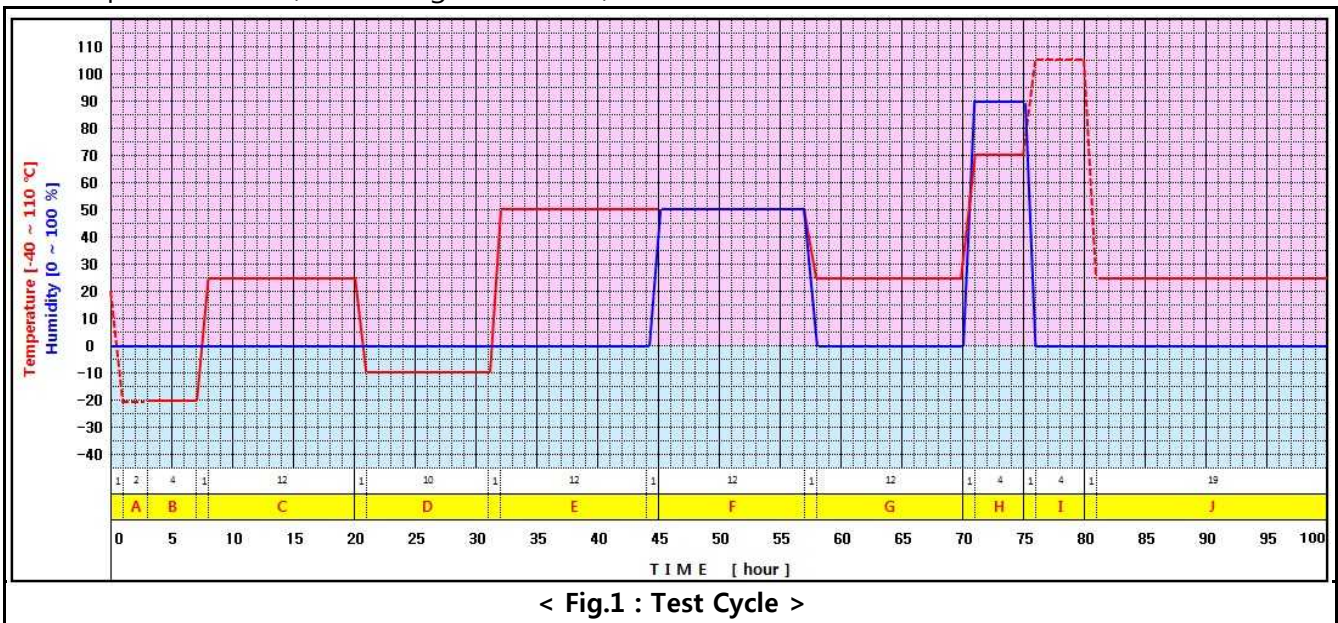
LTS5-24 <(24VDC, 0.21A(100% Load)>, [unit : hrs]			
110Vac Input		220Vac Input	
$T_{amb.} : 25^{\circ}\text{C}$	$T_{amb.} : 50^{\circ}\text{C}$	$T_{amb.} : 25^{\circ}\text{C}$	$T_{amb.} : 50^{\circ}\text{C}$
100,000 hrs	18,000 hrs	87,000 hrs	16,000 hrs

3. Environmental Stress Test(EST)

The purpose of the environment stress test is to ensure reliability by setting in advance the following environment and verified.

- transport process and conservation status
- environmental change conditions that can be applied to the product from the process of the end-user

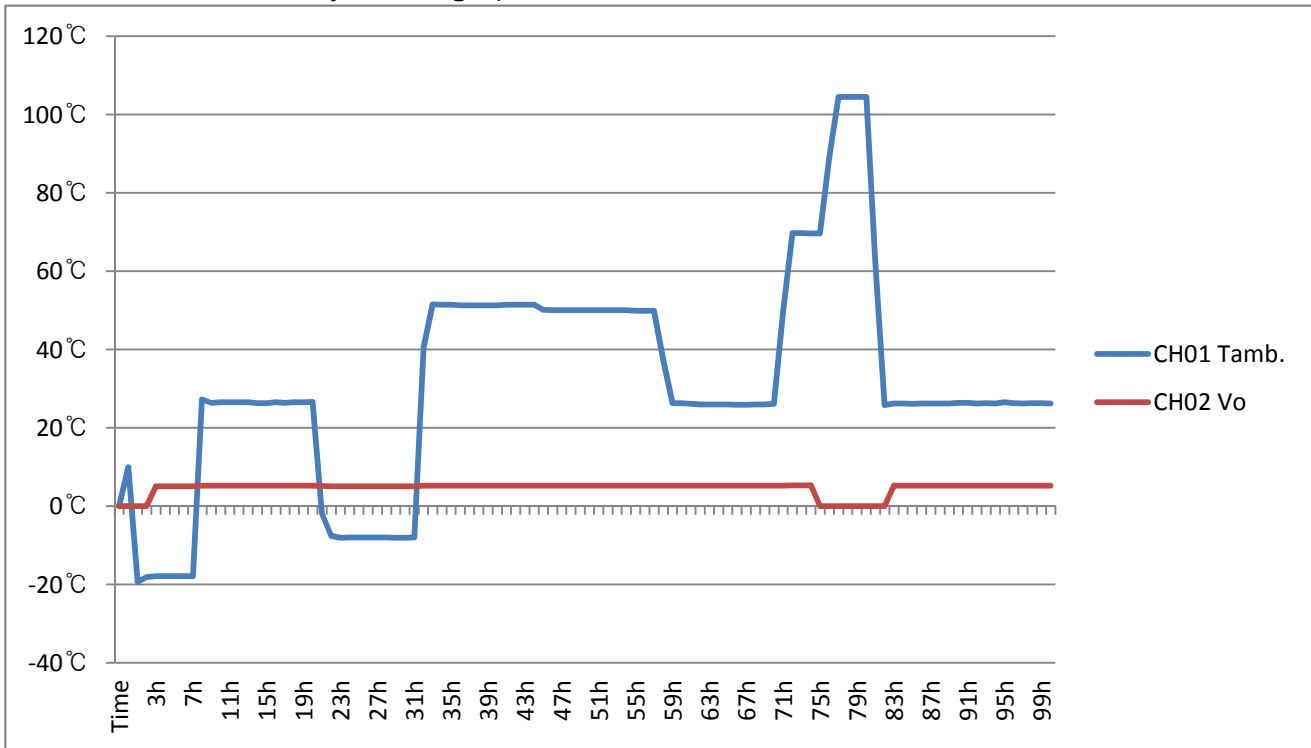
Test cycle consists of 10 segments(total 100 hours). Test results of all segments must meet the specifications. (refer to Fig.1 & Table1)



Segment	Time	Temp.	Humidity	Description	Input 'SW'
A	2 hours	-20°C	0%	Low temperature storage	off
B	4 hours	-20°C	0%	Low temperature operation	on
C	12 hours	25°C	0%	Room temperature operation	on
D	10 hours	-10°C	0%	Low temperature operation	on
E	12 hours	50°C	0%	High temperature operation	on
F	12 hours	50°C	50%	High-temperature & humidity operation	on
G	12 hours	25°C	0%	Room temperature operation	on
H	4 hours	70°C	90%	High-temperature & humidity operation	on
I	4 hours	105°C	0%	High temperature storage	off
J	19 hours	25°C	0%	Room temperature operation	on

3.1. Environmental Stress Test Results

- a. Test Sample : LTS5-05
- b. 100 hours in one cycle test graph



- c. Characteristics test results (@ Input Voltage : 220Vac)

Segment	Test Time	T _{amb.} /Humi.	Output Voltage	Start up	Ripple / Noise	Output Load Condition
A	3h	-40°C / 0%	5.08V	OK	116 [mVp-p]	50% Load
B	10h	25°C / 0%	5.22V	OK	90 [mVp-p]	100% Load
C	24h	-20°C / 0%	5.08V	OK	84 [mVp-p]	100% Load
D	33h	50°C / 0%	5.24V	OK	63 [mVp-p]	100% Load
E	48h	50°C / 50%	5.22V	OK	84 [mVp-p]	100% Load
F	57h	25°C / 0%	5.22V	OK	84 [mVp-p]	100% Load
G	72h	70°C / 90%	5.28V	OK	84 [mVp-p]	100% Load
H	81h	25°C / 0%	5.14V	OK	56 [mVp-p]	100% Load
I	100h	25°C / 0%	5.24V	OK	84 [mVp-p]	100% Load
Test Result			Pass	Pass	Pass	

4. Main Components Δt Test

The purpose of the test is to ensure the reliability and margin by measuring the heating value of the main components.

4.1. LTS5-05 (@ 100% Load)

Test Point \ Test Condition	Vin : 110Vac, 60Hz		Vin : 220Vac, 60Hz	
	T _{amb.}	32.1°C	T _{amb.}	32.0°C
	T _c	Δt	T _c	Δt
Bridge Diode	62.7°C	30.6°C	62.2°C	30.2°C
Input Cap.	70.7°C	38.6°C	71.6°C	39.6°C
PWM IC	74.4°C	42.3°C	76.9°C	44.9°C
Trans Coil	74.1°C	42.0°C	75.4°C	43.4°C
Trans Core	73.6°C	41.5°C	74.8°C	42.8°C
Output Diode	77.0°C	44.9°C	78.0°C	46.0°C

4.2. LTS5-12 (@ 100% Load)

Test Point \ Test Condition	Vin : 110Vac, 60Hz		Vin : 220Vac, 60Hz	
	T _{amb.}	31.3°C	T _{amb.}	32.8°C
	T _c	Δt	T _c	Δt
Bridge Diode	56.3°C	25.0°C	55.9°C	23.1°C
Input Cap.	57.2°C	25.9°C	57.8°C	25.0°C
PWM IC	72.0°C	40.7°C	76.5°C	43.7°C
Trans Coil	73.0°C	41.7°C	75.3°C	42.5°C
Trans Core	69.1°C	37.8°C	71.5°C	38.7°C
Output Diode	69.4°C	38.1°C	71.1°C	38.3°C

4.3. LTS5-15 (@ 100% Load)

Test Point	Test Condition	Vin : 110Vac, 60Hz		Vin : 220Vac, 60Hz	
		T _{amb.}	30.3°C	T _{amb.}	30.7°C
		T _c	Δt	T _c	Δt
Bridge Diode		54.9°C	24.6°C	56.8°C	26.1°C
Input Cap.		60.2°C	29.9°C	63.7°C	33.0°C
PWM IC		65.4°C	35.1°C	73.1°C	42.4°C
Trans Coil		65.8°C	35.5°C	69.3°C	38.6°C
Trans Core		65.2°C	34.9°C	68.6°C	37.9°C
Output Diode		61.9°C	31.6°C	64.0°C	33.3°C

4.4. LTS5-24 (@ 100% Load)

Test Point	Test Condition	Vin : 110Vac, 60Hz		Vin : 220Vac, 60Hz	
		T _{amb.}	34.4°C	T _{amb.}	33.9°C
		T _c	Δt	T _c	Δt
Bridge Diode		54.4°C	20.0°C	54.6°C	20.7°C
Input Cap.		57.9°C	23.5°C	59.5°C	25.6°C
PWM IC		63.2°C	28.8°C	67.9°C	34.0°C
Trans Coil		60.9°C	26.5°C	63.0°C	29.1°C
Trans Core		60.0°C	25.6°C	62.1°C	28.2°C
Output Diode		56.1°C	21.7°C	57.1°C	23.2°C

5. Derating of Semiconductor

Compare T_{jmax} (maximum junction temperature) and T_j and is expressed as a percentage. T_j is the value calculated by the temperature of the case and the power dissipation and the thermal impedance.

- Measuring Components : Bridge Diode, FET, Rectifier diode
- Calculating method of derating ratio

$$\text{Derating Ratio} = \frac{T_j}{T_{j(max)}} \times 100 \text{ [%]}$$

$$T_j = T_c + (R_{\theta(J-C)} \times P_d)$$

T_c : Case Temperature

$R_{\theta(J-C)}$: Thermal impedance between junction and case

P_d : Power dissipation

5.1. LTS5-05

Condition		Vin : 110Vac, 60Hz		Load : 100%	T _{amb.} : 50°C
Components					
BD1 (Bridge Diode)	$T_{j(max)}$: 150 °C	P_d : 0.13 W		$T_j = 88.7$ °C	Derating Ratio = 59.1%
	$R_{\theta(J-A)}$: 62.5 °C/W				
	T_A : 80.6°C				
IC1 (IC + FET)	$T_{j(max)}$: 150 °C	P_d : 0.07 W		$T_j = 95.4$ °C	Derating Ratio = 63.6%
	$R_{\theta(J-C)}$: 30 °C/W				
	T_c : 92.3°C				
D4 (Rectifier Diode)	$T_{j(max)}$: 125 °C	P_d : 0.5 W		$T_j = 98.8$ °C	Derating Ratio = 79.0%
	$R_{\theta(J-C)}$: 12 °C/W				
	T_c : 92.8°C				
Condition		Vin : 220Vac, 60Hz		Load : 100%	T _{amb.} : 50°C
Components					
BD1 (Bridge Diode)	$T_{j(max)}$: 150 °C	P_d : 0.05 W		$T_j = 83.3$ °C	Derating Ratio = 55.6%
	$R_{\theta(J-A)}$: 62.5 °C/W				
	T_A : 80.2°C				
IC1 (IC + FET)	$T_{j(max)}$: 150 °C	P_d : 0.05 W		$T_j = 96.4$ °C	Derating Ratio = 64.3%
	$R_{\theta(J-C)}$: 30 °C/W				
	T_c : 94.9°C				
D4 (Rectifier Diode)	$T_{j(max)}$: 125 °C	P_d : 0.5 W		$T_j = 99.7$ °C	Derating Ratio = 79.8%
	$R_{\theta(J-C)}$: 12 °C/W				
	T_c : 93.7°C				

5.2. LTS5-12

Condition		Vin : 110Vac, 60Hz		Load : 100%	T _{amb.} : 50°C
Components					
BD1 (Bridge Diode)	T _{j(max)} : 150 °C	P _d : 0.12 W		T _j = 82.5 °C	Derating Ratio = 55.0%
	R _{θ(J-A)} : 62.5 °C/W				
	T _A : 75.0°C				
IC1 (IC + FET)	T _{j(max)} : 150 °C	P _d : 0.06 W		T _j = 92.5 °C	Derating Ratio = 61.7%
	R _{θ(J-C)} : 30 °C/W				
	T _C : 90.7°C				
D4 (Rectifier Diode)	T _{j(max)} : 175 °C	P _d : 0.33W		T _j = 86.7 °C	Derating Ratio = 49.5%
	R _{θ(J-C)} : 14 °C/W				
	T _C : 82.1°C				
Condition		Vin : 220Vac, 60Hz		Load : 100%	T _{amb.} : 50°C
Components					
BD1 (Bridge Diode)	T _{j(max)} : 150 °C	P _d : 0.05 W		T _j = 76.2 °C	Derating Ratio = 50.8%
	R _{θ(J-A)} : 62.5 °C/W				
	T _A : 73.1°C				
IC1 (IC + FET)	T _{j(max)} : 150 °C	P _d : 0.05 W		T _j = 95.2 °C	Derating Ratio = 63.5%
	R _{θ(J-C)} : 30 °C/W				
	T _C : 93.7°C				
D4 (Rectifier Diode)	T _{j(max)} : 175 °C	P _d : 0.33W		T _j = 87.9 °C	Derating Ratio = 50.2%
	R _{θ(J-C)} : 14 °C/W				
	T _C : 83.3°C				

5.3. LTS5-15

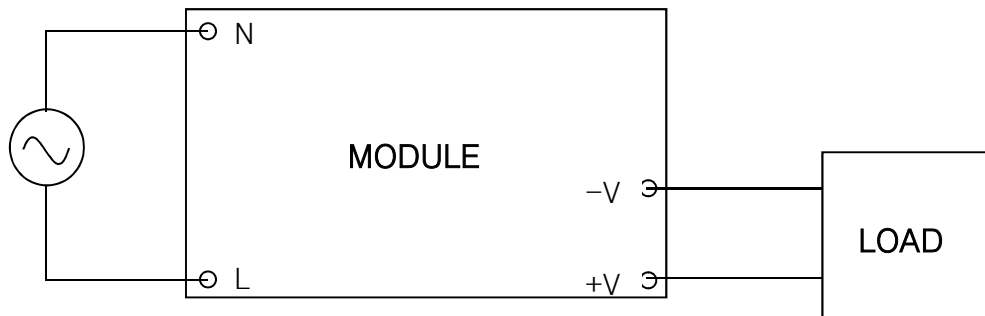
Condition		Vin : 110Vac, 60Hz		Load : 100%	T _{amb.} : 50°C
Components					
BD1 (Bridge Diode)	T _{j(max)} : 150 °C	P _d : 0.13 W		T _j = 82.7 °C	Derating Ratio = 55.1%
	R _{θ(J-A)} : 62.5 °C/W				
	T _A : 74.6°C				
IC1 (IC + FET)	T _{j(max)} : 150 °C	P _d : 0.06 W		T _j = 86.9 °C	Derating Ratio = 57.9%
	R _{θ(J-C)} : 30 °C/W				
	T _C : 85.1°C				
D4 (Rectifier Diode)	T _{j(max)} : 175 °C	P _d : 0.27 W		T _j = 81.3 °C	Derating Ratio = 46.5%
	R _{θ(J-C)} : 14 °C/W				
	T _C : 77.5°C				
Condition		Vin : 220Vac, 60Hz		Load : 100%	T _{amb.} : 50°C
Components					
BD1 (Bridge Diode)	T _{j(max)} : 150 °C	P _d : 0.05 W		T _j = 79.2 °C	Derating Ratio = 52.8%
	R _{θ(J-A)} : 62.5 °C/W				
	T _A : 76.1°C				
IC1 (IC + FET)	T _{j(max)} : 150 °C	P _d : 0.05 W		T _j = 93.9 °C	Derating Ratio = 62.6%
	R _{θ(J-C)} : 30 °C/W				
	T _C : 92.4°C				
D4 (Rectifier Diode)	T _{j(max)} : 175 °C	P _d : 0.27 W		T _j = 82.5 °C	Derating Ratio = 47.1%
	R _{θ(J-C)} : 14 °C/W				
	T _C : 78.7°C				

5.4. LTS5-24

Condition		Vin : 110Vac, 60Hz		Load : 100%	T _{amb.} : 50°C
Components					
BD1 (Bridge Diode)	T _{j(max)} : 150 °C	P _d : 0.11 W		T _j = 76.9 °C	Derating Ratio = 51.3%
	R _{θ(J-A)} : 62.5 °C/W				
	T _A : 70.0°C				
IC1 (IC + FET)	T _{j(max)} : 150 °C	P _d : 0.06 W		T _j = 80.6 °C	Derating Ratio = 53.7%
	R _{θ(J-C)} : 30 °C/W				
	T _C : 78.8°C				
D4 (Rectifier Diode)	T _{j(max)} : 175 °C	P _d : 0.18 W		T _j = 76.5 °C	Derating Ratio = 43.7%
	R _{θ(J-A)} : 27 °C/W				
	T _A : 71.6°C				
Condition		Vin : 220Vac, 60Hz		Load : 100%	T _{amb.} : 50°C
Components					
BD1 (Bridge Diode)	T _{j(max)} : 150 °C	P _d : 0.05 W		T _j = 77.6 °C	Derating Ratio = 51.7%
	R _{θ(J-A)} : 62.5 °C/W				
	T _A : 70.7°C				
IC1 (IC + FET)	T _{j(max)} : 150 °C	P _d : 0.04 W		T _j = 85.2 °C	Derating Ratio = 56.8%
	R _{θ(J-C)} : 30 °C/W				
	T _C : 84.0°C				
D4 (Rectifier Diode)	T _{j(max)} : 175 °C	P _d : 0.18 W		T _j = 77.9 °C	Derating Ratio = 44.5%
	R _{θ(J-A)} : 27 °C/W				
	T _A : 73.0°C				

– Application Sheet

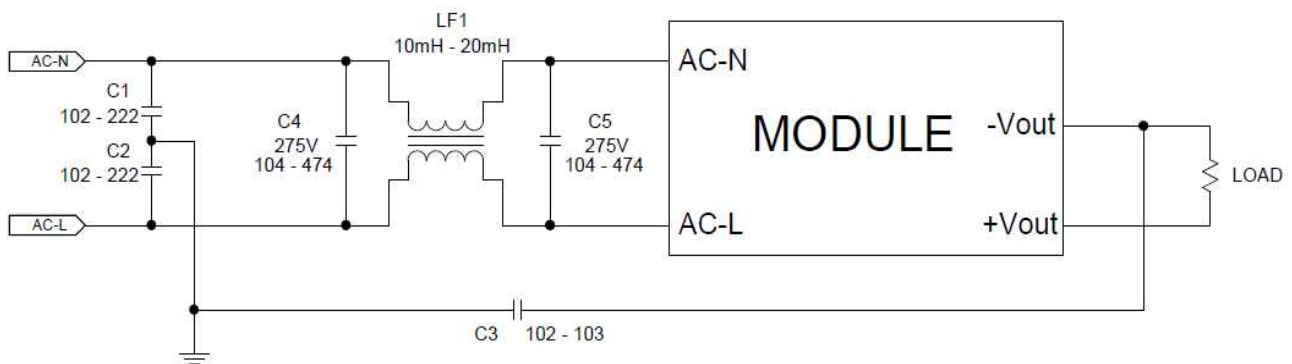
1. Basic Connection



AC INPUT : 90Vac ~ 264Vac (Free Voltage)

※ To avoid excessive voltage drop and improved noise, short and thick wire should be used to connect the load.

2. EMI(Electro Magnetic Interference) Connection



C1, C2	Y-Cap. AC 250V ~ 400V / 1000pF ~ 2200pF
C3	Y-Cap. AC 250V ~ 400V / 1000pF ~ 10000pF
C4, C5	X-Cap. AC 275V / 100nF ~ 470nF
LF1	Common mode line filter 10mH ~ 20mH

3. Output Section

Output Ripple and Noise Measurement Method

The measurement for output ripple and noise are based on normal probe with 20MHz bandwidth scope. Upon measurement of the ripple voltage, make sure that the scope probe leads are not too long. If a precise measurement can be made, the noise occurs from circumference must be reduced.

Regulation

Line Regulation

The line regulation means to the change in output voltage when the input voltage is varied within the input voltage range, at constant load and constant ambient temperature. The measurement point for the output voltage are $\pm V_{out}$ pins respectively.

Load Regulation

The load regulation means to the change in output voltage when the load is changed from minimum load to maximum load, at constant input voltage and constant ambient temperature. The measurement point for the output voltage are $\pm V_{out}$ pins respectively.

4. Protection

Over Current Protection

The LTS series is built into an OCP(Over Current Protection) circuit. When the OCP triggers, the output voltage will fall. If overload condition is removed, the output will automatically recover.

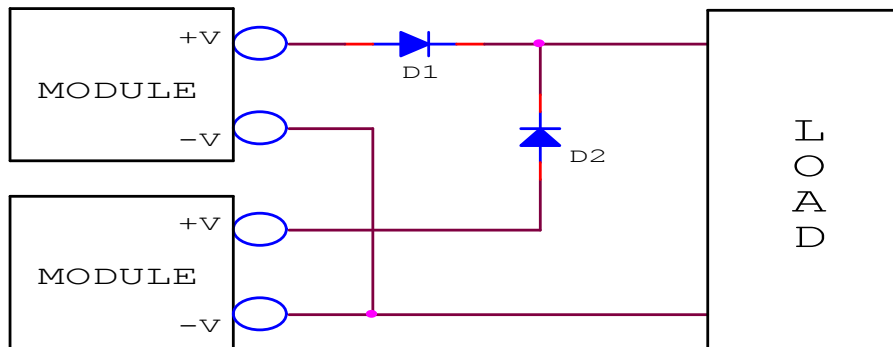
Short Circuit Protection

The LTS series is built into an short circuit protection circuit. It is similar to OCP circuit. When output is short condition, the output voltage will fall. If short condition is removed, the output will automatically recover. However, if the short condition continues damage to the module could occur.

5. Operation Method

Parallel Operation

The module can be operated parallel connection. Refer to diagram as shown below.



Please, you must consider both reverse voltage and forward current of diode, when you choose a diode.

Maximum reverse voltage(V_{rm}) : $V_{rm} > 1.5 \times V_o$

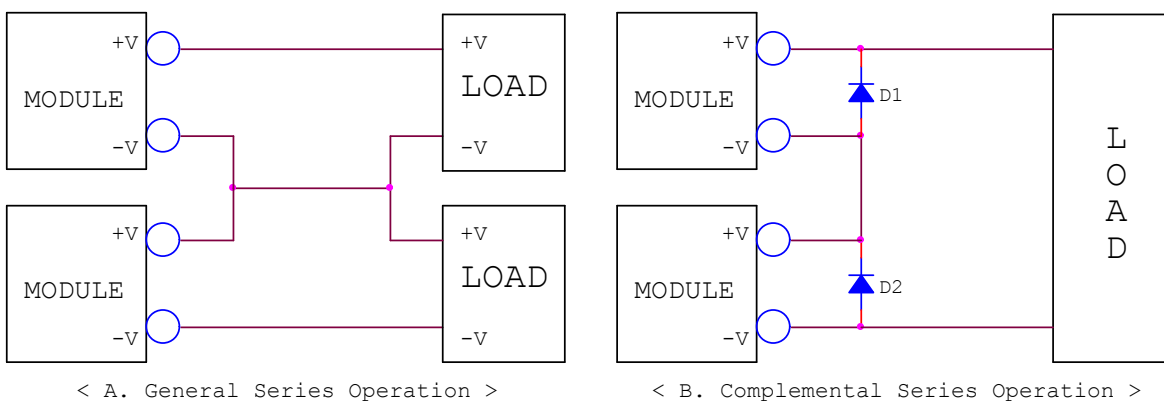
Forward current(I_f) : $I_f > 3 \times I_o$

Also, design a heatsink according to power loss at diode. If you want to reduce power loss, use a schottky barrier diode.

Power loss = $V_f(\text{forward voltage}) \times I_o(\text{output current})$

Series Operation

Series operation is available by connecting the outputs of two or more module as shown below.



< A. General Series Operation >

< B. Complemental Series Operation >

Please, you must consider both reverse voltage and forward current of diode, when you choose a diode.

Maximum reverse voltage(V_{rm}) : $V_{rm} > 1.5 \times V_o$

Forward current(I_f) : $I_f > 3 \times I_o$

Also, design a heatsink according to power loss at diode. If you want to reduce power loss, use a schottky barrier diode.

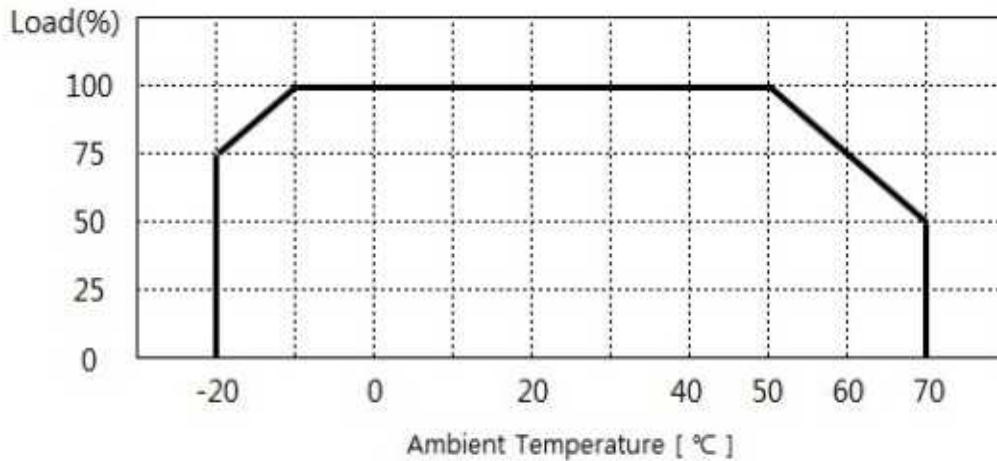
Power loss = $V_f(\text{forward voltage}) \times I_o(\text{output current})$

6. Environment

Temperature

Operation Temperature

The range of ambient temperature in °C over which a module can be operated safely at either rated or derated output power. Refer to derating curve as shown below.



※ Operating Temperature Range : From -20°C to 70°C

< Derating Curve >

Storage Temperature

The range of ambient temperature in °C over which a module may be stored long term without damage. The storage temperature range is from -40°C to 85°C.

Humidity

Operation Humidity

The range of ambient humidity in % over which a module can be operated safely at either rated or derated output power. Refer to derating curve as shown below. The operating humidity range is from 20% to 90%RH.

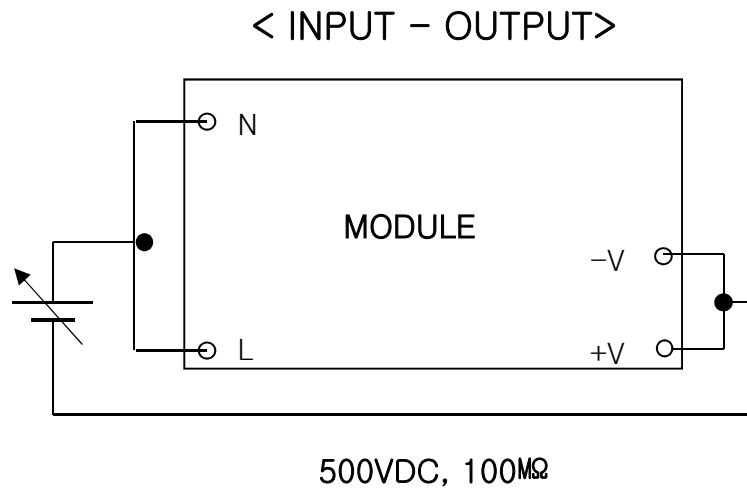
Storage Humidity

The range of ambient humidity in % over which a module may be stored long term without damage. The storage humidity range is from 20% to 90%RH.

7. Isolation

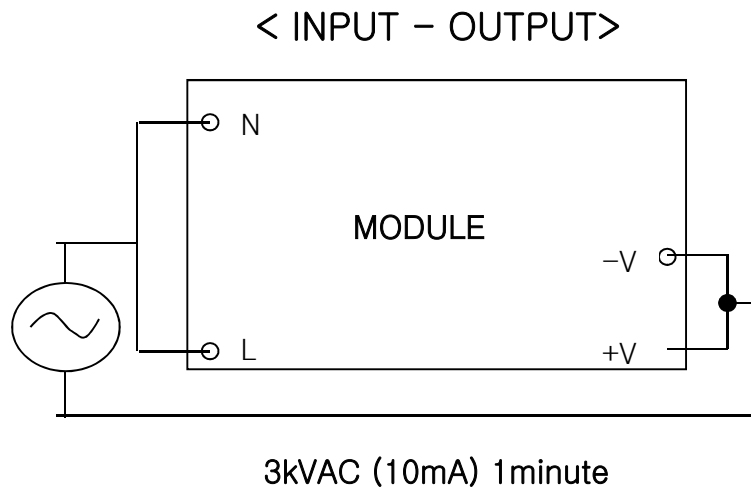
Isolation Resistance

The electrical separation between input and output of a module by means of the power transformer. The isolation resistance is a function of materials and spacings employed throughout the module. Please don't test with a voltage above standard voltage for the Isolation Resistance Test.

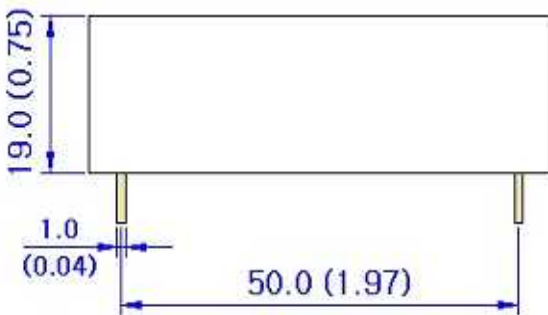
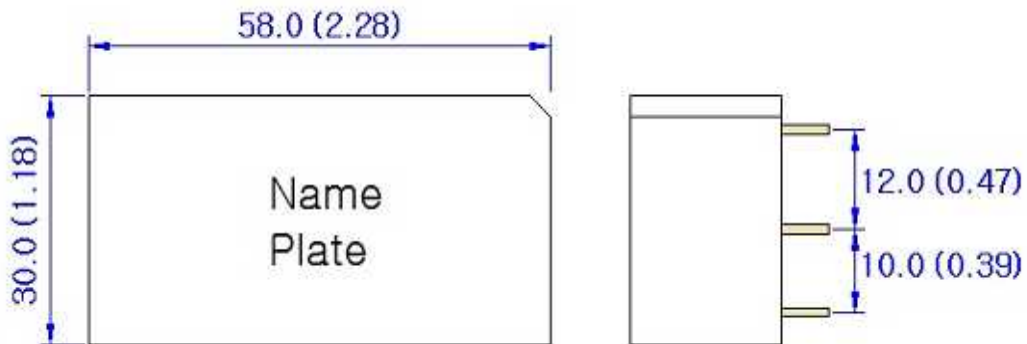


Withstand Voltage

For the withstand voltage test, the applied voltage must be increased gradually from zero to the testing value, and then decreased gradually at shut down. Especially stay away from use of a timer. Where a pulse of several times the applied voltage can be generated.



8. Outline Dimensions <Unit : mm (inch)>



NOTE

1. Unit : mm (inch)
2. Weight : 50g(Max.)
3. Material : PBT

Mounting Hole <Top View>

