

# DATASHEET & RELIABILITY DATA

## SD6 SERIES

(주)오디피

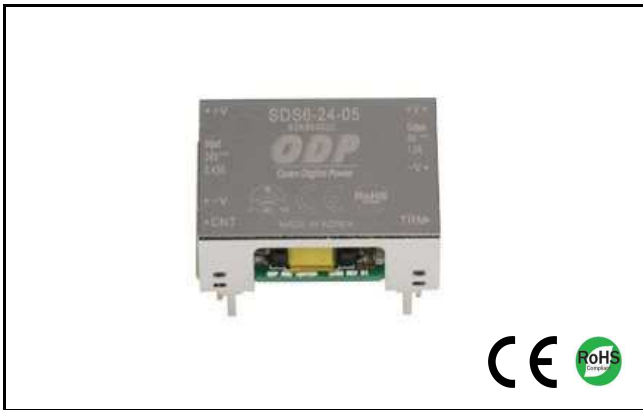
Open Digital Power Corp.

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MAX. Power 6.0W Isolated DC-DC Converter

## SDS6 Series Small Compact Size DC-DC Converter



### Features

- Small Compact Size
- High Efficiency
- Isolated Input – Output
- Wide operating temperature range (-40°C to 85°C)
- Long Life Design (Employ only Ceramic Capacitor)
- Built-in over current protection circuit
- Wide 2 :1 input range
- Adjustable output voltage (single output)
- Safety standard : CE approved
- RoHS compatible design

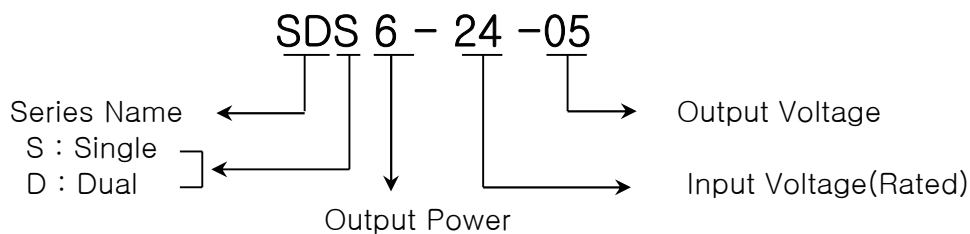
### Applications

- Data and telecommunication
- FA control
- Datacommunication electronic equipments

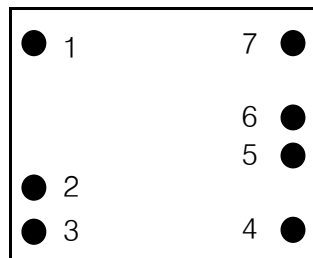
### Environment

- Operating Temperature : -40°C ~ 85°C
- Operating Humidity : 5% ~ 95% RH (Non condensing)
- Storage Temperature : -40°C ~ 105°C
- Cooling : Free-Air Convection
- MTBF : 5.6 x 10<sup>5</sup> hrs

### Model Name Structure



## Pin assignments & Function



<Top View>

### - Single Output Name & Function

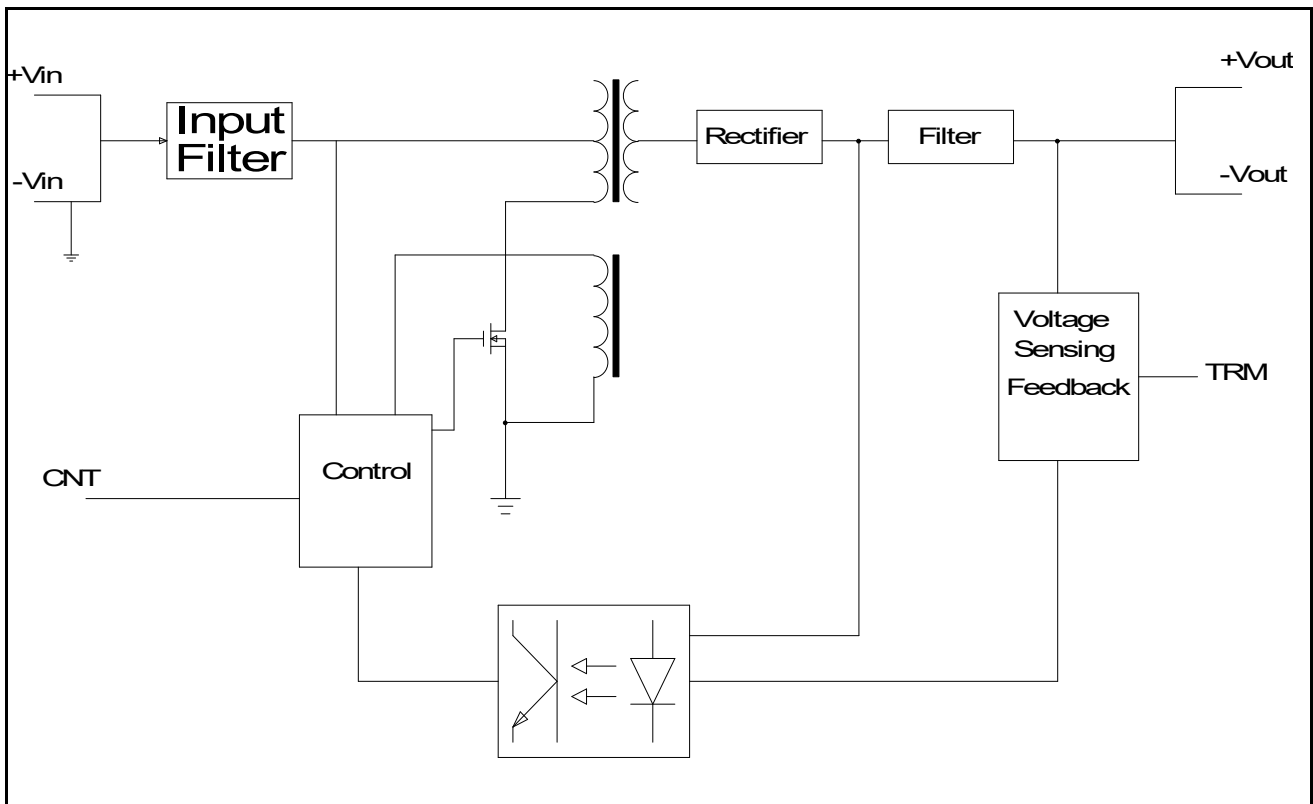
PIN No.	NAME	FUNCTION
1	+Vin	Positive terminal for Vin
2	-Vin	Negative terminal for Vin
3	CNT	Remote on/off Control
4	TRM	Vout variation( $\pm 10\%$ ) by external parts
5	-Vout	Negative terminal for Vout
6	No Pin	
7	+Vout	Positive terminal for Vout

### - Dual Output Name & Function

PIN No.	NAME	FUNCTION
1	+Vin	Positive terminal for Vin
2	-Vin	Negative terminal for Vin
3	CNT	Remote on/off Control
4	-Vout	Negative terminal for Vout
5	No Pin	
6	Com	The common ground of Vout
7	+Vout	Positive terminal for Vout

- Datasheet

1. Internal Circuit Architecture



2. Maximum Ratings

Characteristics		Symbol	Min.	Typ.	Max.	Unit
Input Voltage Continuous	SDS6 - 05 - XX	Vin	4.5	-	9.0	VDC
	SDS6 - 12 - XX		9.0	-	18.0	
	SDS6 - 24 - XX		18.0	-	36.0	
	SDS6 - 48 - XX		36.0	-	76.0	
Operating Ambient Temperature		Ta	-40	-	85	°C
Storage Temperature		Tstg	-40	-	105	°C
Withstand Voltage			-	-	500	Vac

### 3. Electrical Characteristics

#### – Input Section

Ta : 25°C, Vin : Typical Input Voltage

Characteristics		Symbol	Min.	Typ.	Max.	Unit
Operating Voltage Range	SDS6 - 05 - XX	Vin	4.5	5.0	9.0	VDC
	SDS6 - 12 - XX		9.0	12.0	18.0	
	SDS6 - 24 - XX		18.0	24.0	36.0	
	SDS6 - 48 - XX		36.0	48.0	76.0	
Maximum Input Current (Vin : rated, Io : 100%)	SDS6 - 05 - XX	Iin		1.50		A
	SDS6 - 12 - XX			0.63		
	SDS6 - 24 - XX			0.31		
	SDS6 - 48 - XX			0.16		
Maximum No Load Input Current (Vin : rated)	SDS6 - 05 - XX			94		mA
	SDS6 - 12 - XX			43		
	SDS6 - 24 - XX			25		
	SDS6 - 48 - XX			10		

#### – Output Section

Ta : 25°C, Vin : Minimum, Typical, Maximum Input Voltage

Characteristics		Symbol	Min.	Typ.	Max.	Unit
Output Voltage Accuracy		Vo	-	-	±2	%
Regulation	Line Regulation (From min. Vin to max. Vin, constant load)		-	-	±0.5	%
	Load Regulation (From no load to maximum load)		-	-	±1	%
Output Ripple and Noise (Vin : Rated, Io : Max., BW : 20MHz, use the external capacitor(1uF) between +Vo and -Vo)		mVp-p	-	-	1% of Vout	mV (peak to peak)

Characteristics		Symbol	Min.	Typ.	Max.	Unit
Output Current	SDS6 - XX - 3R3 (05)	I <sub>o</sub>	-	-	1.5 (1.2)	A
	SDS6 - XX - 05 (05)		-	-	1.2 (1.0)	
	SDS6 - XX - 12		-	-	0.5	
	SDS6 - XX - 15		-	-	0.4	
Output Current Limit (OCP : Over Current Protection, recovers automatically)			105	-	-	%
Dynamic Load Response (V <sub>in</sub> : rated, I <sub>o</sub> : from 50% to 100%, from 100% to 50%, BW : 20MHz, Freq. : 100Hz, Duty : 0.5, Tr/Tf : 100us use the external capacitor(1uF) between +Vo and -Vo)			-	-	3% of V <sub>out</sub>	mV (peak to peak)
Start - Up Time		T <sub>start</sub>	-	-	10	ms
Turn - on Overshoot			-	-	5	%
Efficiency  (V <sub>in</sub> : Rated, I <sub>o</sub> : Max.)	SDS6 - 05 - 3R3		-	73	-	%
	SDS6 - 05 - 05		-	78	-	
	SDS6 - 05 - 12		-	81	-	
	SDS6 - 05 - 15		-	81	-	
	SDS6 - 12 - 3R3		-	77	-	%
	SDS6 - 12 - 05		-	82	-	
	SDS6 - 12 - 12		-	86	-	
	SDS6 - 12 - 15		-	87	-	
	SDS6 - 24 - 3R3		-	77	-	%
	SDS6 - 24 - 05		-	82	-	
	SDS6 - 24 - 12		-	85	-	
	SDS6 - 24 - 15		-	87	-	
	SDS6 - 48 - 3R3		-	75	-	%
	SDS6 - 48 - 05		-	81	-	
	SDS6 - 48 - 12		-	85	-	
	SDS6 - 48 - 15		-	85	-	

## 4. Isolation Characteristics

Characteristics		Symbol	Min.	Typ.	Max.	Unit
Withstand Voltage (AC500V, 1minute)	Input – Output		–	–	500	Vac
	Input – Case		–	–	500	Vac
	Output – Case		–	–	500	Vac
Isolation Resistance (DC500V at 25°C and 70%RH)	Output – Case	Riso	100	–	–	MΩ

## 5. General Characteristics

Characteristics	Symbol	Min.	Typ.	Max.	Unit
Remote on / off control (CNT Pin, Negative Logic Module on : Logic Low or Short to –Vin Module off : Logic High or open)	CNT				
External Trim Adj. Range (TRM Pin, Vout variation by external parts)	TRM	–10	–	+10	%
Switching Frequency					kHz
MTBF (MIL-HDBK-217F)		5.6 x 10 <sup>5</sup>			hrs
Dimension (W x H x L)		27.5 x 9.1 x 20.0			mm
Weight		–	6.0	–	grams

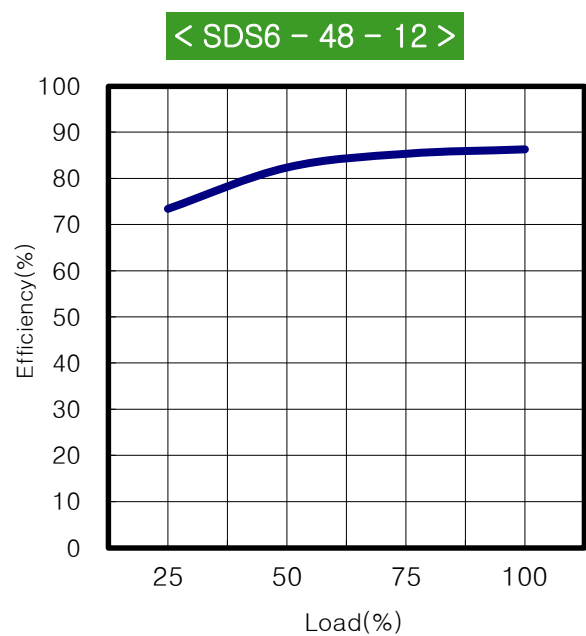
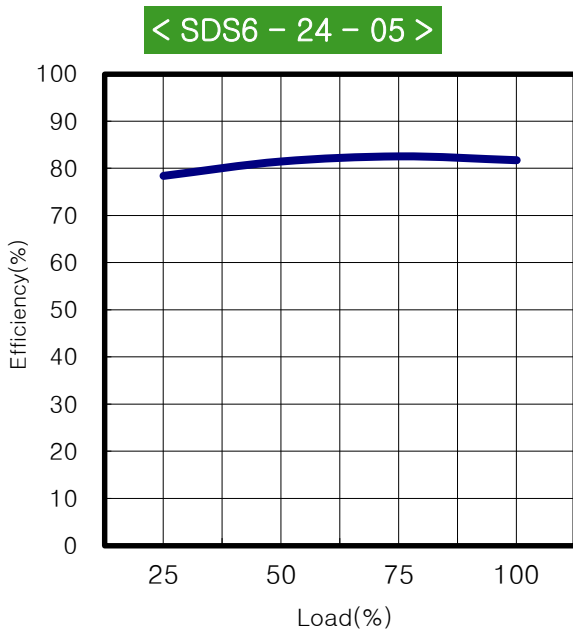
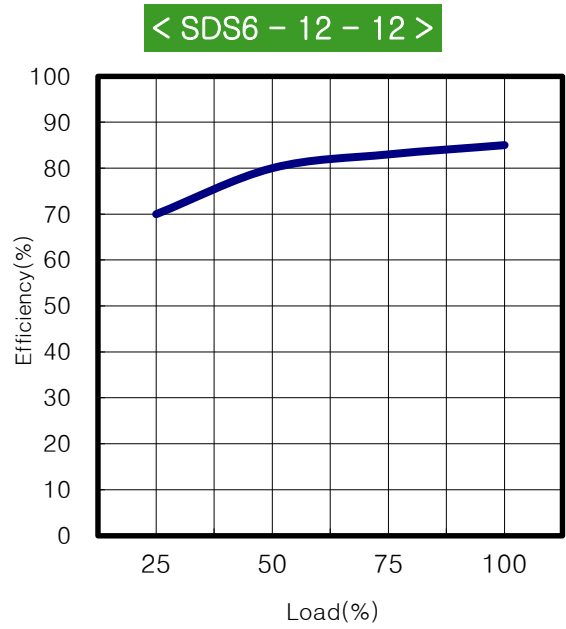
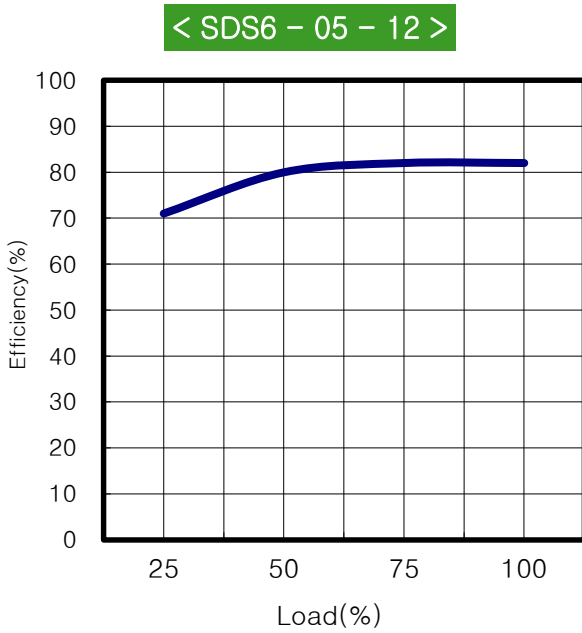
## 6. Environment

Characteristics	Symbol	Min.	Typ.	Max.	Unit
Operating Temperature Range	Ta	–40	–	85	°C
Operating Humidity (non Condensing)		5	–	95	%RH
Storage Temperature	Tstg	–40	–	105	°C



## 7. Characteristics Curves

### Efficiency Curves



## - Reliability Data

### 1. MTBF

Calculating Reliable Values of MTBF

Calculated based on part count reliability projection of MIL-HDBK-217F individual failure rates  $\lambda g$  is given to each part and MTBF is calculated by the count of each part.

Method is :

$$M T B F = \frac{10^6}{\sum_{i=1}^n N i(\lambda g \cdot \pi Q)_i} = \frac{10^6}{\lambda_{equipment}} \quad [ \text{hours} ]$$

For a given equipment environment where :

$\lambda_{equipment}$  = Total equipment failure rate (Failures / 10<sup>6</sup> Hours)

$\lambda g$  = Generic failure rate for the i th generic part (Failures / 10<sup>6</sup> Hours)

$\pi Q$  = Quality factor for the i th generic part (  $\pi Q = 1$  )

$N i$  = Quantity of i th generic part

$n$  = Number of different generic part categories in the equipment

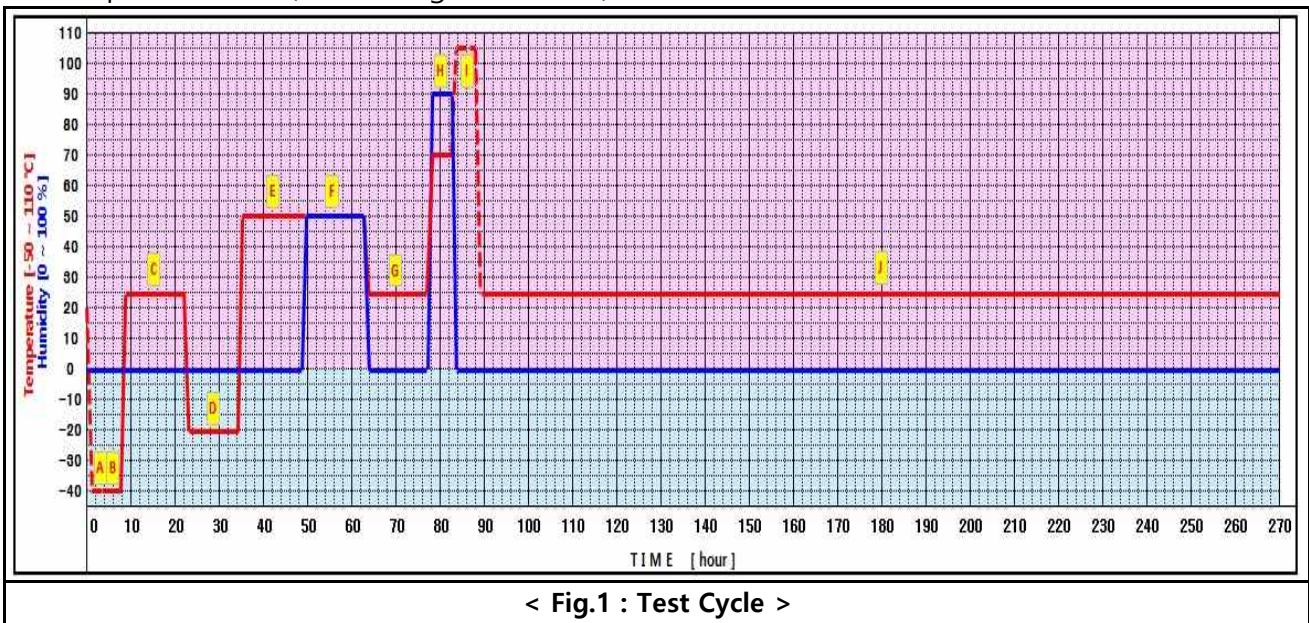
	PART	Number	Failure Rate	Failure Rate
1	Logic IC	0	0.015	-
2	FET	5	0.012	0.0600000
3	Voltage Regulaor	1	0.002	0.0020000
4	Diode (Zener)	3	0.002	0.0060000
5	Diode (FRD)	1	0.069	0.0690000
6	Diode (SBD)	3	0.027	0.0810000
7	Bridge Diode	0	0.066	-
8	LED	0	0.00023	-
9	Varistor	0	0.0013	-
10	Photo-coupler	1	0.07	0.0700000
11	Thyristor	0	0.0022	-
12	Elec.- Cap.	0	0.019	-
13	Ceramic Cap.	3	0.026	0.0780000
14	MLCC	11	0.053	0.5830000
15	Choke coil	2	0.00022	0.0004400
16	Switching transformer	1	0.0042	0.0042000
17	Line Filter	0	0.0044	-
18	Resistor	18	0.0024	0.0432000
19	Resistor Variable	0	0.0024	-
20	Thermister	0	0.0019	-
21	Connertor	0	0.052	-
22	Soldering Point	50	0.0078	0.3900000
23	PCB	1	0.37	0.3700000
24	Fuse	1	0.01	0.0100000
Total Equipment Failure Rate ( $\lambda_{equipment}$ )				1.7668400
MTBF = 10 <sup>6</sup> / $\lambda_{equipment}$ (F/T)				565,982.206
<b>MTBF ≅ 560,000[Hours]</b>				

## 2. Environmental Stress Test(EST)

The purpose of the environment stress test is to ensure reliability by setting in advance the following environment and verified.

- transport process and conservation status
- environmental change conditions that can be applied to the product from the process of the end-user

Test cycle consists of 10 segments(total 270 hours). Test results of all segments must meet the specifications. (refer to Fig.1 & Table1)

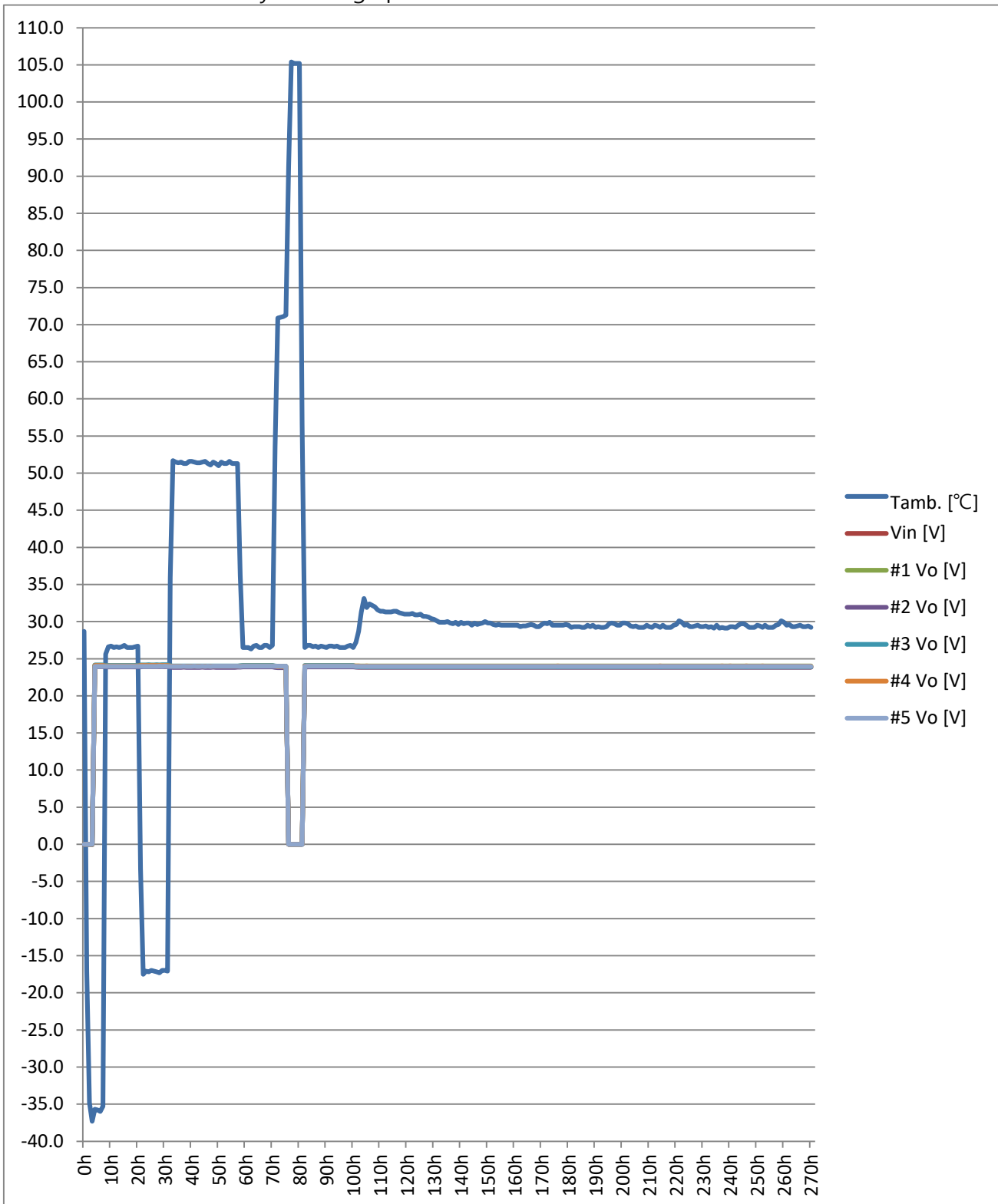


Segment	Time	Temp.	Humidity	Description	Input 'SW'
A	2 hours	-40°C	0%	Low temperature storage	off
B	4 hours	-40°C	0%	Low temperature operation	on
C	12 hours	25°C	0%	Room temperature operation	on
D	10 hours	-20°C	0%	Low temperature operation	on
E	12 hours	50°C	0%	High temperature operation	on
F	12 hours	50°C	50%	High-temperature & humidity operation	on
G	12 hours	25°C	0%	Room temperature operation	on
H	4 hours	70°C	90%	High-temperature & humidity operation	on
I	4 hours	105°C	0%	High temperature storage	off
J	189 hours	25°C	0%	Room temperature operation	on

**< Table1 : Segment Description >**

### 2.1. Environmental Stress Test Results

- a. Test Sample : SDD6-24-1212 (from '-Vo' to '+Vo = 24VDC)
- b. 270 hours in one cycle test graph



c. Characteristics test results (@ Input Voltage : 24VDC, Output Load : 0.23A(92% of Full load))

Segment	Test Time	T <sub>amb</sub> /Humi.	Output Voltage	Start up	Ripple / Noise [ mVp-p ]	Sapmle No.
A	3h	-40°C / 0%	24.14V	OK	26 / 41	# 1
			23.95V	OK	36 / 49	# 2
			24.17V	OK	33 / 43	# 3
			24.11V	OK	34 / 47	# 4
			23.96V	OK	42 / 52	# 5
B	7h	-40°C / 0%	24.11V	OK	25 / 39	# 1
			23.94V	OK	35 / 48	# 2
			24.16V	OK	31 / 41	# 3
			24.10V	OK	33 / 45	# 4
			23.96V	OK	40 / 50	# 5
C	10h	25°C / 0%	24.06V	OK	20 / 31	# 1
			23.90V	OK	28 / 38	# 2
			24.08V	OK	25 / 33	# 3
			24.04V	OK	26 / 36	# 4
			23.96V	OK	32 / 40	# 5
D	24h	-20°C / 0%	24.12V	OK	23 / 35	# 1
			23.93V	OK	32 / 43	# 2
			24.18V	OK	28 / 37	# 3
			24.09V	OK	29 / 41	# 4
			23.97V	OK	36 / 45	# 5
E	33h	50°C / 0%	24.01V	OK	19 / 30	# 1
			23.84V	OK	27 / 37	# 2
			24.03V	OK	24 / 32	# 3
			23.99V	OK	25 / 35	# 4
			23.97V	OK	31 / 39	# 5
F	48h	50°C / 50%	24.00V	OK	19 / 30	# 1
			23.85V	OK	27 / 36	# 2
			24.04V	OK	24 / 32	# 3
			23.99V	OK	25 / 34	# 4
			23.98V	OK	31 / 37	# 5
G	57h	25°C / 0%	24.06V	OK	20 / 32	# 1
			23.89V	OK	29 / 40	# 2
			24.10V	OK	26 / 38	# 3
			24.04V	OK	27 / 37	# 4
			23.98V	OK	33 / 42	# 5
H	72h	70°C / 90%	23.95V	OK	19 / 35	# 1
			23.84V	OK	27 / 43	# 2
			23.98V	OK	24 / 37	# 3
			23.94V	OK	25 / 41	# 4
			23.98V	OK	30 / 48	# 5
I	81h	25°C / 0%	24.06V	OK	20 / 36	# 1
			23.88V	OK	29 / 44	# 2
			24.09V	OK	26 / 34	# 3
			24.03V	OK	27 / 36	# 4
			23.98V	OK	33 / 46	# 5
J	270h	25°C / 0%	24.02V	OK	20 / 32	# 1
			23.87V	OK	29 / 36	# 2
			23.91V	OK	26 / 36	# 3
			24.01V	OK	27 / 38	# 4
			23.99V	OK	33 / 40	# 5
Test Result			Pass	Pass	Pass	

### 3. Main Components Δt Test

The purpose of the test is to ensure the reliability and margin by measuring the heating value of the main components.

#### 3.1. SDD6-24-1212 (@ 100% Load)

Test Point	Test Condition	Vin : 18VDC		Vin : 24VDC	
		T <sub>amb.</sub>	29.9°C	T <sub>amb.</sub>	28.5°C
		T <sub>c</sub>	Δt	T <sub>c</sub>	Δt
FET		67.0°C	<b>37.1°C</b>	64.3°C	<b>35.8°C</b>
Trans Coil		73.6°C	<b>43.7°C</b>	71.0°C	<b>42.5°C</b>
Trans Core		71.6°C	<b>41.7°C</b>	71.8°C	<b>43.3°C</b>
Output Diode		59.3°C	<b>29.4°C</b>	62.5°C	<b>34.0°C</b>

### 4. Derating of Semiconductor

Compare T<sub>jmax</sub>(maximum junction temperature) and T<sub>j</sub> and is expressed as a percentage. T<sub>j</sub> is the value calculated by the temperature of the case and the power dissipation and the thermal impedance.

- Measuring Components : FET, Rectifier diode
- Calculating method of derating ratio

$$\text{Derating Ratio} = \frac{T_j}{T_{j(max)}} \times 100 [\%]$$

$$T_j = T_c + (R_{\theta(j-c)} \times P_d)$$

T<sub>c</sub> : Case Temperature

R<sub>θ(j-c)</sub> : Thermal impedance between junction and case

P<sub>d</sub> : Power dissipation

#### 4.1. SDD6-24-1212

Components	Condition	Vin : 18VDC Load : 100%		T <sub>amb.</sub> : 50°C
		T <sub>j(max)</sub>	P <sub>d</sub>	
Q1 (FET)	T <sub>j(max)</sub> : 150 °C	P <sub>d</sub> : 0.59 W		<b>Derating Ratio = 66.3%</b>
	R <sub>θ(j-c)</sub> : 21 °C/W	T <sub>j</sub> = 99.5 °C		
	T <sub>c</sub> : 87.1°C			
D4 (Rectifier Diode)	T <sub>j(max)</sub> : 150 °C	P <sub>d</sub> : 0.138 W		<b>Derating Ratio = 54.1%</b>
	R <sub>θ(j-c)</sub> : 13 °C/W	T <sub>j</sub> = 81.2 °C		
	T <sub>c</sub> : 79.4°C			



## 5. Abnormal Test

Page 31 of 31  
Report Reference No.: ITYA0726529



Product Service

Clause	Requirement – Test						Result – Remark	Verdict
5.3	TABLE: fault condition tests							P
	ambient temperature (°C) .....						23 °C	—
	model/type of power supply .....						SDD10, SDD6	—
	manufacturer of power supply .....						Open Digital Power	—
	rated markings of power supply .....						5 – 48V/d.c.	—
component No.	fault	test voltage (V)	test time	fuse No.	fuse current (A)	result		
Reverse input	-	5/12/24/48	1 sec	F1	6- 1,5	Immediately fuse(F1) opened. No hazard.		
C1	s/c	5/12/24/48	1 sec	F1	6- 1,5	Immediately fuse(F1) opened. No hazard.		
PC1 #3-4	s/c	5/12/24/48	10 min	F1	6- 1,5	Immediately output shutdown. No hazard.		
PC1 #1-2	s/c	5/12/24/48	1 sec	F1	6- 1,5	Immediately fuse(F1) opened. No hazard.		
Output	s/c	5/12/24/48	30 min	F1	6- 1,5	Immediately output shutdown. No hazard.		
Q1 # 1-8	s/c	5/12/24/48	1 sec	F1	6- 1,5	Immediately fuse(F1) opened. No hazard.		
T1 # 1-5 for SDD10 series	s/c	5/12/24/48	30 min	F1	6- 1,5	Normal operation. No hazard.		
T1 # 1-10 for SDD6 series	s/c	5/12/24/48	30 min	F1	6- 1,5	Normal operation. No hazard.		
supplementary information								
s/c: short circuit, #: pin No.,								

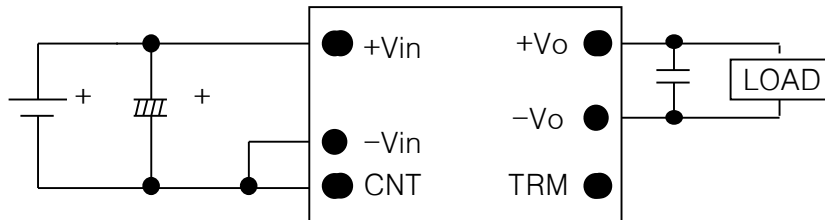
**Note** Before placing the products in the different countries the manufacturer has to guarantee that:

1. Operating instructions and warnings are written in an accepted language of the certain country.
2. The equipment is in compliance with the national standards of the certain country.

Test Report EN/IEC 60950-1b Rev. 00 / 2003-04

## - Application Sheet

### 1. Basic Connection



### 2. Input Section

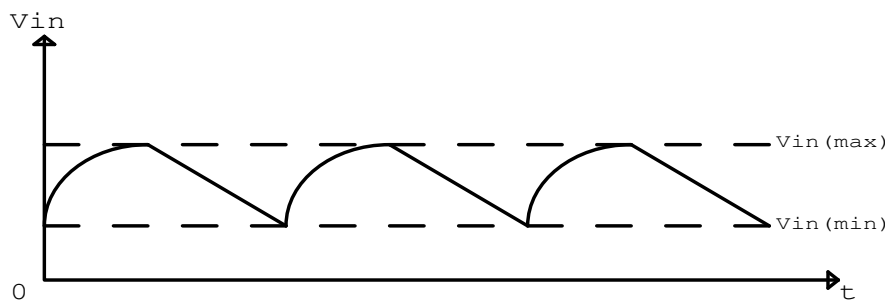
#### - Input fuse

In order to comply with safety requirements, SDS series has a fuse(Slow Blow Type) built in.

	SDS1R5 Series	SDS3 Series	SDS6 Series	SDS10 Series
5V	2A	3A	5A	6A
12V	1A	2.5A	2.5A	4A
24V	1A	1A	2A	2.5A
48V	1A	1A	1.5A	2A

#### - Unstable Input

Input voltage is comprised of both the DC voltage(average rectified voltage)and the peak to peak ripple voltage. Peak to peak ripple voltage should be minimized so that the input voltage is within the standard input voltage range as follows.



< Unstable Input >

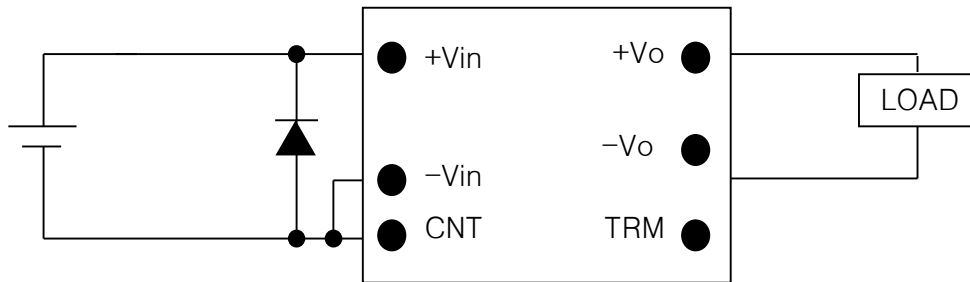


**- Battery Input**

When using a battery as the input power supply, make sure that the maximum and minimum input voltage do not away out of the standard input voltage range.

**- Input Reverse-polarity voltage protection**

Accidently reversing the input connections could damage the module. Thus. If the connections may be accidently reversed. Use a protective diode and an input fuse as shown below.



**- Remote On/Off Control(CNT) (Except SDS1R5 Series)**

Without switching the input on/off, the output can be enabled and disabled using this function. This function is useful for sequence control when building multiple output power supplies. This control circuit is on the input side using the CNT pin. Ground of CNT pin is the input -V terminal. When not using this function, short CNT to input -V terminal.

CNT level for -Vin		OUTPUT
Low level	Short to -Vin	ON
High level	Open	OFF

< Negative Logic on/off Control >

### 3. Output Section

**- Output Ripple and Noise Measurement Method**

The measurement for output ripple and noise are based on normal probe with 20MHz bandwidth scope. Upon measurement of the ripple voltage, make sure that the scope probe leads are not too long. If a precise measurement can be made, the noise occurs from circumference must be reduced.

**- Line Regulation**

The line regulation means to the change in output voltage when the input voltage is varied within the input voltage range, at constant load and constant ambient temperature. The measurement point for the input and output voltage are  $\pm V_{in}$  pins,  $\pm V_{out}$  pins respectively.

**- Load Regulation**

The load regulation means to the change in output voltage when the load is changed from minimum load to maximum load, at constant input voltage and constant ambient temperature. The measurement point for the input and output voltage are  $\pm V_{in}$  pins,  $\pm V_{out}$  pins respectively.

**- Output Voltage adjustment (TRM)**

The output voltage can be varied within  $\pm 10\%$  of the standard output voltage when use the external parts-resistors and variable resistor.

External Resistors :

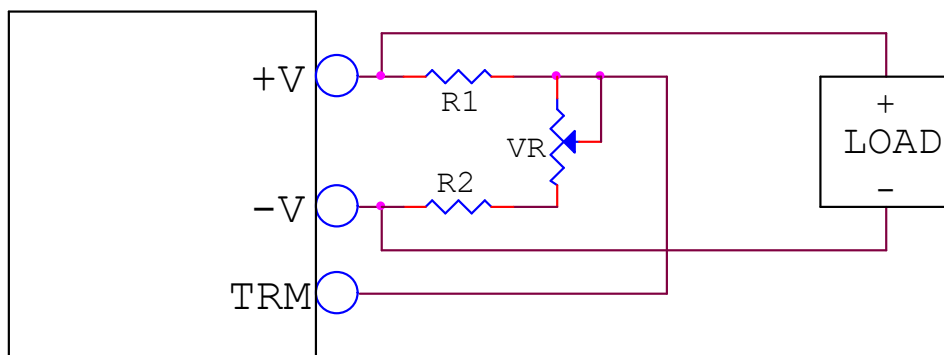
Resistance tolerance  $\pm 5\%$

Variable Resistor(VR) :

Total resistance toloatace  $\pm 20\%$

Remaining Resistance : Value less than 1%

Vo	R1	R2	VR
3.3V	1.5k $\Omega$	680 $\Omega$	1k $\Omega$
5V	1k $\Omega$	680 $\Omega$	1k $\Omega$
12V	3.9k $\Omega$	680 $\Omega$	1k $\Omega$
15V	5.6k $\Omega$	750 $\Omega$	1k $\Omega$
$\pm 12(24V)$	12k $\Omega$	1k $\Omega$	1k $\Omega$
$\pm 15(30V)$	15k $\Omega$	1k $\Omega$	1k $\Omega$



< Trim Method >

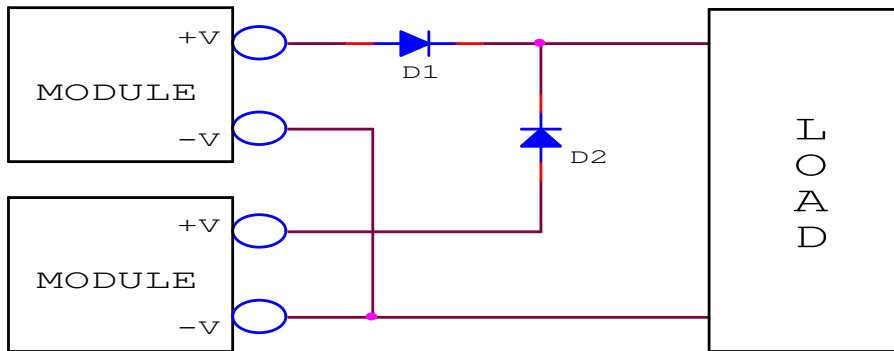
**- Over Current Protection**

The SDS series is built into an OCP(Over Current Protection) circuit. When the OCP triggers, the output voltage will be fall. If overload condition is removed, the output will automatically recover.

## 4. Operation Method

### Parallel Operation

The module can be operated parallel connection. Refer to diagram as shown below.



Please, you must consider both reverse voltage and forward current of diode, when you choose a diode.

Maximum reverse voltage( $V_{rm}$ ) :  $V_{rm} > 1.5 \times V_o$

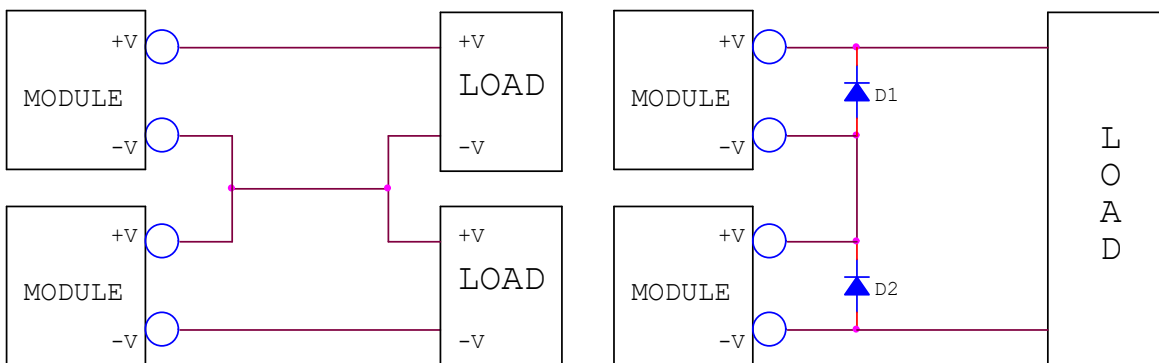
Forward current( $I_f$ ) :  $I_f > 3 \times I_o$

Also, design a heatsink according to power loss at diode. If you want to reduce power loss, use a schottky barrier diode.

Power loss =  $V_f(\text{forward voltage}) \times I_o(\text{output current})$

### Series Operation

Series operation is available by connecting the outputs of two or more module as shown below.



< A. General Series Operation >

< B. Complemental Series Operation >

Please, you must consider both reverse voltage and forward current of diode, when you choose a diode.

Maximum reverse voltage( $V_{rm}$ ) :  $V_{rm} > 1.5 \times V_o$

Forward current( $I_f$ ) :  $I_f > 3 \times I_o$

Also, design a heatsink according to power loss at diode. If you want to reduce power loss, use a schottky barrier diode.

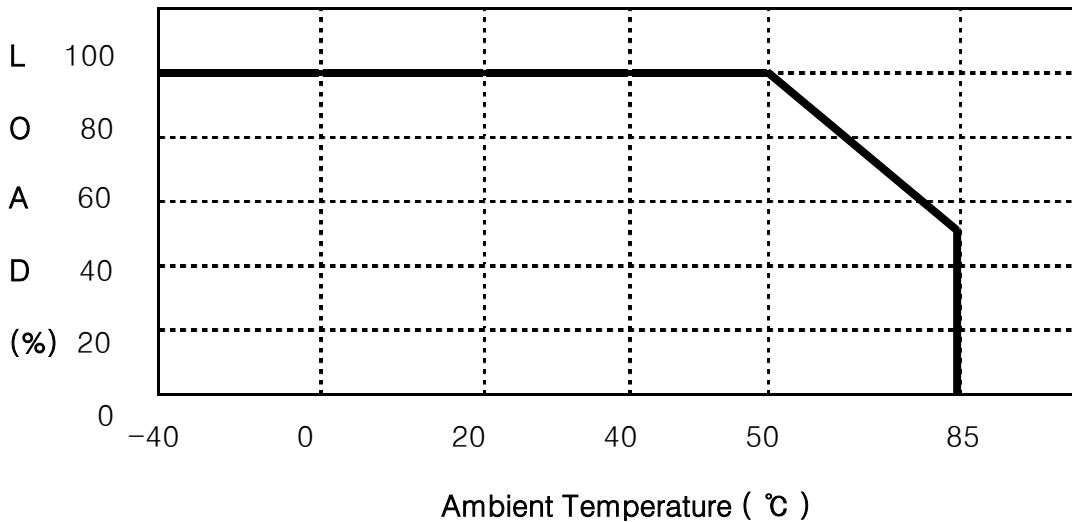
Power loss =  $V_f(\text{forward voltage}) \times I_o(\text{output current})$

## 5. Environment

### - Temperature

#### Operation Temperature

The range of ambient temperature in °C over which a module can be operated safely at either rated or derated output power. Refer to derating curve as shown below.



※ Operating Temperature Range : From -40°C to 85°C

< Derating Curve >

#### Storage Temperature

The range of ambient temperature in °C over which a module may be stored long term without damage. The storage temperature range is from -40°C to 105°C.

### - Humidity

#### Operation Humidity

The range of ambient humidity in % over which a module can be operated safely at either rated or derated output power. Refer to derating curve as shown below. The operating humidity range is from 5% to 95%RH.

#### Storage Humidity

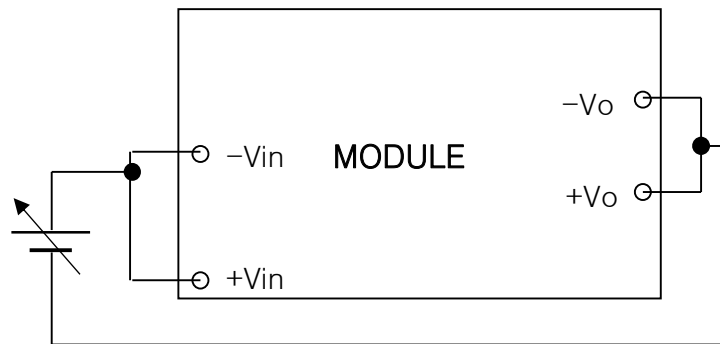
The range of ambient humidity in % over which a module may be stored long term without damage. The storage humidity range is from 5% to 95%RH.

## 6. Isolation

### Isolation Resistance

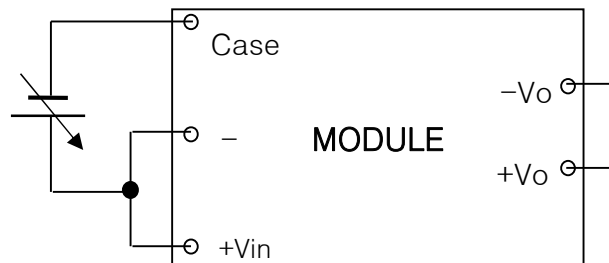
The electrical separation between input and output of a module by means of the power transformer. The isolation resistance is a function of materials and spacings employed throughout the module. Please don't test with a voltage above standard voltage for the Isolation Resistance Test.

#### < INPUT - OUTPUT >



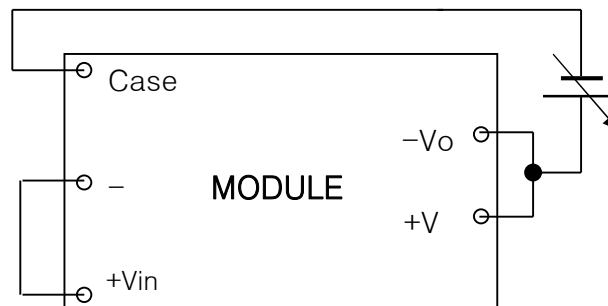
500VDC, 100MΩ

#### < INPUT - Case >



500VDC, 100MΩ

#### < OUTPUT - FRG >

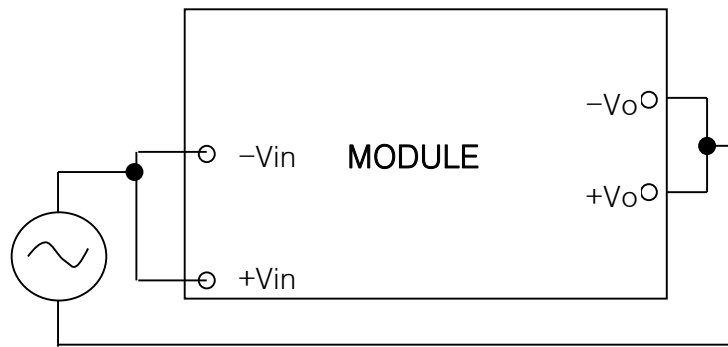


500VDC, 70MΩ

**Withstand Voltage**

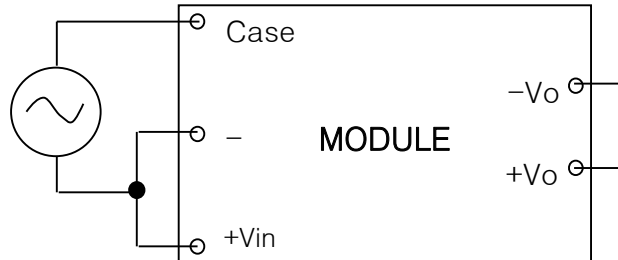
For the withstand voltage test, the applied voltage must be increased gradually from zero to the testing value, and then decreased gradually at shut down. Especially stay away from use of a timer. Where a pulse of several times the applied voltage can be generated.

< INPUT - OUTPUT >



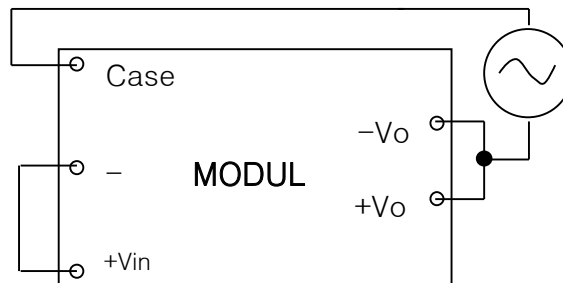
500VAC (10mA) 1minute

< INPUT - FRG >



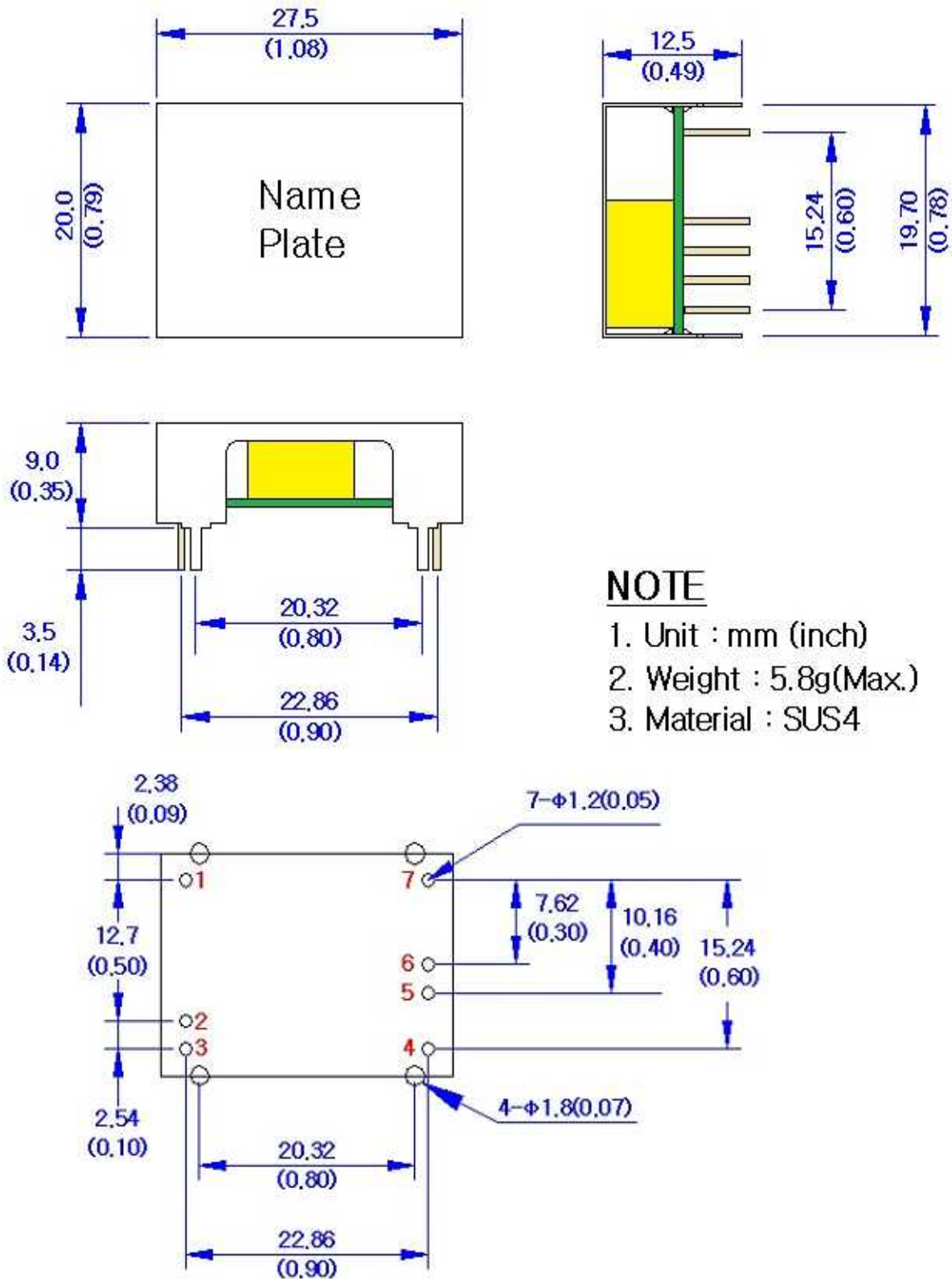
500VAC (10mA) 1minute

< OUTPUT - FRG >



500VAC (10mA) 1minute

7. Outline Dimensions <Unit : mm (inch)>



**NOTE**

1. Unit : mm (inch)
2. Weight : 5.8g(Max.)
3. Material : SUS4